## 34980A

## Data acquisition system

## Introduction

The 34980A multifunction switch/measure unit provides functionality that is easy to set up and use with a fast startup time. The 34980A helps you lower your test cost and accelerate your test-system integration and development.


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## High-performance Data Acquisition System Provides a Low-cost, Highly Flexible Measurement Platform

Suppose you use automated test equipment for design validation or manufacturing. In that case, you now have a cost-effective solution for many test system applications. The 34980A multifunction switch/measure unit provides functionality that is easy to set up and use with a fast startup time. The 34980A helps you lower your cost of tests and accelerate your test-system integration and development

The 34980A handles system switching up to 26.5 GHz and provides basic measurements and system control. It also offers DMM measurements, counter/ totalizer functionality, digital I/O with pattern capabilities, and analog outputs with basic waveforms-all in one low-cost, compact box. And with its standard connectors and software drivers, computer-standard I/O, and Web browser interface, the 34980A easily integrates into electronic functional test and data acquisition systems.

## Flexible Switching, Measurements, and System Control

The 34980A accommodates up to 8 plug-in modules to give you the flexibility you need. Choose from 21 different modules to define your own configuration. You can buy what you need now and add to it or reconfigure it as your requirements change.

Whether you are measuring temperature, AC or DC voltage, resistance, frequency, current, or custom measurements, the 34980A offers the functionality you need in a single box. Switch in different measurements with high-performance signal switching up to 300 V with no external signal conditioning required. Choose between different switch types and topologies with frequency ranges from DC to 26.5 GHz. The 34980A offers high-density multiplexers for scanning multiple channels, matrices for connecting multiple points at one time, and general-purpose switches for simple control and high power needs.

Use the 34980A to individually route signals or monitor multiple signals over a specified period of time monitor a single channel or multiple channels, set alarms, and identify irregularities.

The 34980A offers flexible choices for system control. You can control external devices such as microwave switches, attenuators, solenoids, and power relays. Or you can use the digital inputs to sense limit-switch and digital-bus status.

## Optimized for Test Systems

The 34980A has the performance you need for medium- to high-density switching/measurement applications such as design verification, functional test, and data acquisition. Your signals are switched to the right measurement device without compromising signal integrity. Switch your signals to the optional internal DMM and achieve optimal throughput on switch closure time. Or, if you prefer, you can easily connect to external instruments such as DMMs, scopes, power supplies, and more. What's more, with the built-in Ethernet interface, you can control the 34980A and collect data from anywhere on the network. For a complete modular solution, use the 34980A together with PXI modular products when more capability is required.

The rugged instrument comes with a variety of system-ready features:

- Web browser interface shows settings at a glance and provides remote access and control when using a browser that supports Java applets.
- Self-guiding front panel to configure, troubleshoot or view data
- Low EMI and efficient system cooling
- Heavy-duty cabling and connection options
- Flexible rack mounting options
- Relay counters help predict end-of-life
- In-rack calibration for reduced maintenance time
- DMM measurement accuracies include the switch for simple calculations

Make system connections easily and quickly with simple, reliable connection options:

- Built-in Ethernet, USB 2.0, and GPIB connectivity
- Low-cost, standard 50- or 78-pin Dsub connectors and cables
- Detachable terminal blocks with strain relief
- Mass interconnect solutions

Keysight Technologies, Inc. IO Libraries Suite enables you to quickly establish an error-free connection between your PC and instruments-regardless of vendor. The IO Libraries provide robust instrument control and enable you to work with the software development environment you choose.

The 34980A provides four 2-wire internal analog buses for easy signal routing. You can route your measurements directly to the internal DMM, or you can connect to external instruments through the analog bus connector on the rear of the mainframe. And since you have four 2-wire buses, you can dedicate one bus for use with the internal DMM and use the other three buses for module extensions or additional signal routing between modules which can reduce wiring complexity.

You can define up to 500 switch sequences to control complex signal routing and the order of switch closures. Assign a sequence, give it a name and then execute it with the name you create. Switch sequences are downloaded and stored in the instrument for ease of programming and increased throughput.

External trigger capabilities make it easy for you to time and synchronize measurements and other events and control when to begin or end an acquisition.

## Measurements You Can Trust

Get proven performance from Keysight instruments with the resolution, repeatability, speed, and accuracy you've come to expect.

The 34980A offers built-in signal conditioning and modular flexibility. When you use it with the internal DMM, you can configure each channel independently for the measurements you choose. It includes a variety of features that give you confidence in your measurements:

- $61 / 2$ digits of resolution with $.004 \%$ of accuracy with DC voltage measurements
- Alarms per channel—high limit, low limit, or both
- Math functions—use Mx+B for custom linear conversions and converting raw inputs
- Built-in thermocouple reference for temperature measurements (34921T)
- Time-stamped readings
- Add more formulas with BenchVue DAQ

The integrated DMM is mounted inside the mainframe and does not consume any of the eight useravailable slots. You can access the DMM through any switch module that connects to the analog bus, or directly from the analog bus connector on the rear of the mainframe. The internal DMM gives you the flexibility to measure 11 types of inputs:

- Temperature with thermocouples, RTDs, or thermistors (with 34921A)
- DC and AC voltage
- 2- and 4-wire resistance
- Frequency and period
- DC and AC current

You can control the DMM directly or configure it to work in conjunction with the switches. Each switch channel can be configured independently for measurement functions, scale factors, and alarm limits. Advanced measurement features such as offset compensation, variable integration time, and delay are also selectable on a per-channel basis.

The DMM inputs are shielded and optically isolated from the 34980A's earth-referenced circuitry and computer interface, and as a result, you get up to 300 V of input isolation. This is important for reducing ground-loops and common-mode voltage errors associated with long wiring runs and floating sources.

Simple DMM calibration is accomplished with just the analog bus connection on the rear panel of the mainframe. You don't need to remove the mainframe from the rack or dedicate a channel for calibration.

## Modules Provide Flexible System Stimulus and Control

System control—with analog outputs, open-collector digital outputs, clock generation, and isolated FormC relays for controlling external devices. Additionally, with the microwave switch/attenuator driver, highfrequency switches and attenuators can be efficiently controlled externally to the 34980A mainframe.

Analog sources-output either voltage or current. You can configure the 4-channel isolated D/A converter as a point-to-point arbitrary waveform generator that lets you define up to 500,000 points per waveform.

Digital patterns-send or receive digital data from your device under test. With onboard memory, you can output communication protocols and bitstreams or monitor digital input patterns and interrupt when a user-defined pattern is detected.

## Standard Interfaces Take the Hassle Out of Connecting to Your PC

Standard Ethernet, USB, and GPIB interfaces are included in every mainframe. Use one of the built-in interfaces that are already available in your computer, or if you prefer, GPIB is still available.

- USB offers the quickest and easiest connection scheme-it's perfect for small systems and bench connections.
- Ethernet offers high-speed connections that allow for remote access and control. Choose a local area network to filter out unwanted LAN traffic and speed up the I/O throughput. Or take advantage of the remote capabilities and distribute your tests worldwide. Use the graphical Web browser to monitor, troubleshoot, or debug your application remotely.
- GPIB has many years of proven reliability for instrument communication and can be used in existing GPIB-based test systems.


## Remote Access and Control

The built-in Web browser interface provides remote access and control of the instrument via a Java applet-enabled browser such as Internet Explorer version 11. Using the Web interface, you can set up, troubleshoot, and maintain your system remotely:

- View and modify instrument setup
- Open, close, or monitor switches
- Send SCPI commands
- Define and execute switch scans and switch sequences
- View error queue
- Get status reports on relay counts, firmware revisions, and more

Additionally, since the Web interface is built into the instrument, you can access it on any operating system that supports the Web browser without having to install any special software. Password protection and LAN lock-out are also provided to limit access. The Web interface makes it easy to set up, troubleshoot, and maintain your system remotely.

Work with your choice of software so you can save time and preserve your software and hardware investments. Program directly with SCPI, or use IVI or LabVIEW software drivers that provide compatibility with the most popular development environments and tools:

- Keysight VEE Pro
- National Instruments LabVIEW, LabWindows/CVI, TestStand, and Switch Executive
- Microsoft Visual Studio.NET, C/C++, and Visual Basic 6


## BenchVue Data Acquisition Control and Analysis

The BenchVue software platform enables you to easily connect to and control the 34980A using the BenchVue DAQ application. This application enables you to quickly configure channels, log data, and visualize measurements for 11 of the 21 plug-in modules. Simply identify the measurements you want to acquire, initiate the process and see the data displayed in real-time. The rich set of colorful graphics provides many options for analyzing and displaying your data. You can specify multiple channels per graph or send collected data to multiple graphs. You can use strip charts with markers and alarm indications or histograms with statistics. And, of course, you can use BenchVue to easily move data to other applications for further analysis or for inclusion in your presentations and reports.


Figure 1. BenchVue DAQ

## Power and Flexibility to Get Your Job Done



Store up to 500,000 readings with timestamps
 connector kits

## Analog Bus / Internal DMM

| Max volts | Switch/Carry <br> current | BW <br> $(M H z)$ | Power <br> (VA) | Volt-Hertz <br> limit | Max <br> Transients ${ }^{4}$ | Scan <br> ch/sec | Thermal <br> offset | Comments |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $\pm 300 \mathrm{~V}^{1,2}$ | $1 \mathrm{~A} / 2 \mathrm{~A}$ | 45 MHz | $60 \mathrm{VA}^{3}$ | $10^{\wedge} 8$ | 1000 Vpk | N/A | N/A | Config as <br> 2- or 4-wire |

Notes:
Pollution Degree $1 \pm 300$ Vrms or VDC; Pollution Degree $2 \pm 100$ Vrms or VDC.
DC or AC RMS voltage, channel-to-channel or channel-to-earth.
Limited to 6 W of channel resistance power loss per module.
4. Transient overvoltage is defined as a short duration overvoltage of a few milliseconds or less, oscillatory or nonoscillatory, usually highly damped (IEC 61010-1).

## Mix and Match 34980A Modules to Create Your Own Custom Configuration

The 34980A mainframe holds up to eight plug-in modules. Mix and match them to create a custom system to meet your switching and system control needs. You can easily add or replace modules as your needs change

| Module | Description | Max volts ${ }^{5}$ | Switch/Carry current | BW (MHz) | Scan ch/sec | Thermal offset | Comments |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Multiplexer modules |  |  |  |  |  |  |  |
| 34921A | 40-channel armature multiplexer w/low thermal offset | $\pm 300 \mathrm{~V}^{1,6}$ | 1A/2A | 45 MHz | 100 | $<3 \mu \mathrm{~V}$ | Temperature reference 4 current channels Config as 2- or 4-wire |
| 34922A | 70-channel armature multiplexer | $\pm 300 \mathrm{~V}^{1,6}$ | 1A/2A | 25 MHz | 100 | $<3 \mu \mathrm{~V}$ | Config as 2- or 4-wire |
| 34923A | 40/80-channel reed multiplexer | $\pm 150 \mathrm{~V} 2,7$ | 0.5A/1.5A | 45 MHz | 500 | $<50 \mu \mathrm{~V}$ | Config as 1-, 2- or 4-wire |
| 34924A | 70-channel reed multiplexer | $\pm 150 \mathrm{~V}^{2,7}$ | 0.5A/1.5A | 25 MHz | 500 | $<50 \mu \mathrm{~V}$ | Config as 2- or 4-wire |
| 34925A | 40/80-channel optically isolated FET multiplexer | $\pm 80 \mathrm{~V}_{\text {peak }}{ }^{6}$ | 0.02A | 1 MHz | 1000 | $<3 \mu \mathrm{~V}$ | Config as 1-, 2- or 4-wire |
| Matrix modules |  |  |  |  |  |  |  |
| 34931A | Dual $4 \times 8$ armature matrix | $\pm 300 \mathrm{~V}^{1,6}$ | 1A/2A | 30 MHz | 100 | $<3 \mu \mathrm{~V}$ | Backplane expandable |
| 34932A | Dual $4 \times 16$ armature matrix | $\pm 300 \mathrm{~V}^{1,6}$ | 1A/2A | 30 MHz | 100 | $<3 \mu \mathrm{~V}$ | Backplane expandable |
| 34933A | Dual/Quad 4x8 reed matrix | $\pm 150 \mathrm{~V}^{2,7}$ | 0.5A/1.5A | 30 MHz | 500 | $<50 \mu \mathrm{~V}$ | Backplane expandable Config as 1- or 2-wire |
| 34934A | Quad 4x32 reed matrix | $\pm 100 \mathrm{~V}_{\text {peak }}{ }^{8}$ | 0.5A/0.5A | 20MHz | 500 | $<50 \mu \mathrm{~V}$ | Row expansion kit. Config as 1- or 2-wire |
| General-purpose modules |  |  |  |  |  |  |  |
| 34937A | 28-channel Form C and 4-channel Form A | $\begin{aligned} & \pm 300 \text { V }^{1,6} \\ & \pm 250 \text { VAC }^{3,6} \end{aligned}$ | $\begin{aligned} & 1 \mathrm{~A} / 2 \mathrm{~A} \\ & 5 \mathrm{~A} / 7 \mathrm{~A} \end{aligned}$ | 10 MHz | N/A | $\begin{aligned} & <3 \mu \mathrm{~V} \\ & <3 \mu \mathrm{~V} \end{aligned}$ |  |
| 34938A | 20-channel 5-amp Form A | $\pm 250 \mathrm{VAC}^{3,6}$ | 5A/7A | 1 MHz | N/A | $<3 \mu \mathrm{~V}$ |  |
| 34939A | 64-channels Form A | $\pm 100 \mathrm{~V}_{\text {peak }}{ }^{4,6}$ | 1A/2A | 10 MHz | N/A | $<3 \mu \mathrm{~V}$ |  |
| Notes: |  |  |  |  |  |  |  |
| 1. | Pollution Degree $1 \pm 300$ Vrms or VDC; Pollution Degree $2 \pm 100$ Vrms or VDC |  |  |  |  |  |  |
| 2. | Pollution Degree $1 \pm 150$ Vpeak; Pollution Degree $2 \pm 100$ Vpeak |  |  |  |  |  |  |
| 3. | Pollution Degree $1 \pm 250$ Vrms or 30 VDC; Pollution Degree $2 \pm 100$ Vrms or 30 VDC |  |  |  |  |  |  |
| 4. | Pollution Degree 1 and Pollution Degree $2 \pm 100$ Vrms or VDC |  |  |  |  |  |  |
| 5. | Transient overvoltage is defined as a short duration overvoltage of a few milliseconds or less, oscillatory or nonoscillatory, usually highly damped (IEC 61010-1). |  |  |  |  |  |  |
| 6. | 1000 Vpk transients. |  |  |  |  |  |  |
| 7. | 750Vpk transients. |  |  |  |  |  |  |
| 8. | 450 Vpk transients. |  |  |  |  |  |  |


| Module | Description | Insertion loss | Isolation | Freq range | VSWR | Input impedance | Comments |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RF and mic | wave modules |  |  |  |  |  |  |
| 34941A | Quad $1 \times 450$ ohm 3 GHz RF multiplexer | 0.6 dB | $>58 \mathrm{~dB}$ | 3 GHz | <1.25 | $50 \Omega$ | @ 1 GHz |
| 34942A | Quad 1x4 75 ohm 1.5 GHz RF multiplexer | 0.6 dB | $>60 \mathrm{~dB}$ | 1.5 GHz | <1.35 | $75 \Omega$ | @ 1 GHz |
| 34945A / 34945EXT | Microwave switch/attenuator driver | Can drive up to 64 external switch coils; 32 SPDT switches, 8 multiport switches, 8 attenuators, or your own combination. Expand with additional 34945EXTs |  |  |  |  |  |
| 34946A | Dual $1 \times 2$ SPDT terminated microwave switch | $\begin{aligned} & <0.42 \mathrm{~dB} \\ & <0.69 \mathrm{~dB} \\ & <0.8 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & >85 \mathrm{~dB} \\ & >67 \mathrm{~dB} \\ & >60 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & 4 \mathrm{GHz} \text { or } 20 \\ & \mathrm{GHz} \\ & 26.5 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & <1.15 \\ & <1.30 \\ & <1.6 \end{aligned}$ | $50 \Omega$ | @ 4 GHz <br> @ 20 GHz <br> @ 26.5 GHz |
| 34947A | Triple 1x2 SPDT unterminated microwave switch | $\begin{aligned} & <0.42 \mathrm{~dB} \\ & <0.69 \mathrm{~dB} \\ & <0.8 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & >85 \mathrm{~dB} \\ & <0.69 \mathrm{~dB} \\ & <0.8 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & 4 \mathrm{GHz} \text { or } \\ & 20 \mathrm{GHz} \\ & 26.5 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & <1.15 \\ & <1.30 \\ & <1.6 \end{aligned}$ | $50 \Omega$ | @ 4 GHz <br> @ 20 GHz <br> @ 26.5 GHz |


| Module | Description | Comment |
| :--- | :--- | :--- |
| System control modules | Eight 8-bit digital I/O channels with programmable polarity, thresholds up to 5 V , with handshaking <br> protocols, and pattern memory. Two 10 MHz frequency counter and programmable clock output to 20 MHz |  |
| 34950 A | 64-bit digital I/O with memory <br> and counter | 4-channel isolated D/A <br> converter with waveform <br> memory | | Output DC voltage up to $\pm 16 \mathrm{~V}$ or DC current up to $\pm 20 \mathrm{~mA}$. |
| :--- |
| Output waveforms with a 200 kHz update rate and 16 bits of resolution. |
| Use on-board memory to create point-to-point waveforms with more than 500,000 points. |

## 34980A Multiplexer Switch Modules

The 34980A multiplexer modules can be used to connect one of many different points to a single point. You can connect to an external instrument or scan multiple analog signals to the internal DMM.

Choose from the following features:

- 1-wire, 2-wire, or 4-wire configurations
- High voltage-up to $300 \mathrm{~V}, 1 \mathrm{~A}$
- High density-70 2-wire or 80 1-wire channels
- Scan up to $1000 \mathrm{ch} / \mathrm{sec}$ with the 34925 A
- Bandwidths up to 45 MHz
- Temperature measurements with built-in thermocouple reference junction (34921T)
- AC or DC current measurements without external shunts
- Flexible connections via standard 50- or 78- pin Dsub cables or detachable terminal blocks


Figure 2. 34921A 40-channel armature multiplexer with low thermal offset (bank 2)

|  | Voltage | Current | Freq/ | $\mathbf{\Omega 2 -}$ | $\mathbf{\Omega 4 -}$ | Thermocouple | RTD <br> 2-Wire | RTD |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | AC/DC | AC/DC | Period | Wire | Wire |  | Thermistor |  |  |
| 34921A Armature Multiplexer | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes |
| 34922A Armature Multiplexer | Yes | No | Yes | Yes | Yes | Yes | Yes | Yes | Yes |
| 34923A Reed Multiplexer (2-wire) | Yes | No | Yes | Yes | Yes | Yes | Yes | Yes | Yes |
| 34923A Reed Multiplexer (1-wire) | Yes | No | Yes | Yes | No | Yes | Yes | No | Yes |
| 34924A Reed Multiplexer | Yes | No | Yes | Yes | Yes | Yes | Yes | Yes | Yes |
| 34925A FET Multiplexer (2-wire) | Yes | No | Yes | Yes | Yes | Yes | No | Yes | No |
| 34925A FET Multiplexer (1-wire) | Yes | No | Yes | Yes | No | Yes | No | No | No |

Note: See user's guide for additional information

Multiple multiplexers can connect to the built-in analog buses, allowing you to scan up to 560 2-wire channels or 640 1-wire channels in a single mainframe. The 34921A also offers 4 channels for directly measuring current. Or, if you need more current channels, shunts can be added to the terminal block for easy current measurements.

The multiplexer modules feature break-before-make connections to ensure that no two signals are connected to each other during a scan. Or, if you prefer, you can control switching manually to create your own switch configuration. All the multiplexer switches have a relay counter to help predict when relays need to be replaced.

Note: The 34923A and 34924A have 100-ohm input protection resistors that limit current and protect the reed relays.


Figure 3. 34925A 40/80-channel optically isolated FET mux (shown in 1-wire mode bank 2)


Figure 4. 34923A 40 channel reed multiplexer (bank 1 shown)

| Multiplexer switch modules | 34921A | 34922A | 34923A | 34924A | 34925A |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Channels/configurations | 40 2-wire <br> 204 -wire <br> 4-current <br> 1.5 A Fused | 70 2-wire 354 -wire | 80 1-wire <br> 40 2-wire <br> 204 -wire | 70 2-wire 35 4-wire | 80 1-wire 40 2-wire 204 -wire |
| Switch type | Armature latching | Armature latching | Reed | Reed | Optically isolated FET |
| Input characteristics (per channel) |  |  |  |  |  |
| Max volts | $\pm 300 \mathrm{~V}^{1,13}$ | $\pm 300 \mathrm{~V}^{1,13}$ | $\pm 150 \mathrm{~V}$ peak ${ }^{2,14}$ | $\pm 150 \mathrm{~V}$ peak ${ }^{2} 14$ | $\pm 80 \mathrm{~V}$ peak ${ }^{2}$ |
| Max current (DC, AC RMS) Switch current | 1A | 1 A | $0.5 \mathrm{~A}^{5} / 0.05 \mathrm{~A}^{11}$ | $0.5 \mathrm{~A}^{5} / 0.05 \mathrm{~A}^{11}$ | $0.02 \mathrm{~A}^{8}$ |
| Carry current | 2 A | 2 A | $1.5 \mathrm{~A}^{5} / 0.05 \mathrm{~A}^{11}$ | $1.5 \mathrm{~A}^{5} / 0.05 \mathrm{~A}^{11}$ |  |
| Power (VA) ${ }^{6}$ | 60 VA | 60 VA | 10 VA | 10 VA | 1.6 VA |
| Volt-Hertz limit | $10^{\wedge} 8$ | 10^8 | 10^8 | 10^8 | $10^{\wedge} 7$ |
| Initial closed channel res ${ }^{3}$ | $<1.5 \Omega^{12}$ | $<1.5 \Omega{ }^{12}$ | $\begin{aligned} & <1.5 \Omega^{5} / 2000^{11} \\ & \text { nominal } \end{aligned}$ | $\begin{aligned} & <1.5 \Omega^{5 / 200}{ }^{11} \\ & \text { Nominal } \end{aligned}$ | $<700 \Omega^{11}$ |
| General specifications |  |  |  |  |  |
| Offset voltage ${ }^{3}$ | $<3 \mu \mathrm{~V}$ | $<3 \mu \mathrm{~V}$ | $\begin{aligned} & <50 \mu \mathrm{~V} \\ & <100 \mathrm{uV} \text { 1-wire } \end{aligned}$ | < 50 uV | $<3 \mu \mathrm{~V}$ |
| DC Isolation (ch-ch, ch-earth) | $>10 \mathrm{G} \Omega$ | $>10 \mathrm{G} \Omega$ | $>10 \mathrm{G} \Omega$ > | $10 \mathrm{G} \Omega$ | $>10 \mathrm{G} \Omega$ |
| Leakage current ${ }^{3}$ | N/A | N/A | N/A | N/A | $20 \mathrm{nA}{ }^{9}$ |
| T/C cold junction accuracy ${ }^{3,10}$ | $<1^{\circ} \mathrm{C}$ | N/A | N/A | N/A | N/A |
| AC characteristics |  |  |  |  |  |
| Bandwidth at terminal block ${ }^{4}$ | 45 MHz | 25 MHz | $45 \mathrm{MHz} / 4 \mathrm{MHz}$ 10 MHz 1-wire | $25 \mathrm{MHz}{ }^{5} / 4 \mathrm{MHz}{ }^{11}$ | 1 MHz |
| $\begin{aligned} & \text { Crosstalk at terminal block (ch-ch) } \\ & 300 \mathrm{kHz} \end{aligned}$ | - 75 dB | - 75 dB | - 75 dB | - 75 dB | Not recommended for RF signal switching |
| 1 MHz 20 MHz 45 MHz | $\begin{aligned} & -75 \mathrm{~dB} \\ & -50 \mathrm{~dB} \\ & -40 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & -75 \mathrm{~dB} \\ & -50 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & -75 \mathrm{~dB} \\ & -50 \mathrm{~dB} \\ & -40 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & -70 \mathrm{~dB} \\ & -45 \mathrm{~dB} \end{aligned}$ |  |
| Capacitance at terminal block HI-LO LO - earth | $\begin{aligned} & 150 \mathrm{pF} \\ & 150 \mathrm{pF} \end{aligned}$ | $\begin{aligned} & 250 \mathrm{pF} \\ & 200 \mathrm{pF} \end{aligned}$ | $\begin{aligned} & 130 \mathrm{pF} \\ & 120 \mathrm{pF} \end{aligned}$ | $\begin{aligned} & 200 \mathrm{pF} \\ & 170 \mathrm{pF} \end{aligned}$ | ```100 pF (00 pF 1-wire)``` |
| General Characteristics |  |  |  |  |  |
| Relay life, typical No-load $10 \mathrm{~V}, 100 \mathrm{~mA}$ <br> Rated load | $\begin{aligned} & 100 \mathrm{M} \\ & 10 \mathrm{M} \\ & 100 \mathrm{k} \end{aligned}$ | $\begin{aligned} & 100 \mathrm{M} \\ & 10 \mathrm{M} \\ & 100 \mathrm{k} \end{aligned}$ | $\begin{aligned} & 1000 \mathrm{M} \\ & 10 \mathrm{M} \\ & 10 \mathrm{k} \end{aligned}$ | $\begin{aligned} & 1000 \mathrm{M} \\ & 10 \mathrm{M} \\ & 10 \mathrm{k} \end{aligned}$ | Unlimited within banks |
| Scanning speeds ${ }^{7}$ | $100 \mathrm{ch} / \mathrm{sec}$ | $100 \mathrm{ch} / \mathrm{sec}$ | $500 \mathrm{ch} / \mathrm{sec}$ | $500 \mathrm{ch} / \mathrm{sec}$ | $1000 \mathrm{ch} / \mathrm{sec}$ |
| Open/ close time, typical | $4 \mathrm{~ms} / 4 \mathrm{~ms}$ | $4 \mathrm{~ms} / 4 \mathrm{~ms}$ | $0.5 \mathrm{~ms} / 0.5 \mathrm{~ms}$ | $0.5 \mathrm{~ms} / 0.5 \mathrm{~ms}$ | $0.25 \mathrm{~ms} / 0.25 \mathrm{~ms}$ |
| Analog bus backplane | Yes | Yes | Yes | Yes | Yes |
| Notes: |  |  |  |  |  |
|  |  |  |  |  |  |
| 1. DC or AC RMS voltage, channel-to-channel or channel-to-earth |  |  |  |  |  |
| 3. Into analog bus. System errors are included in the internal DMM measurement accuracy specifications |  |  |  |  |  |
| 4. $50 \Omega$ source, $50 \Omega$ load, differential measurements verified with 4-port network analyzer (Sdd21) |  |  |  |  |  |
| 5. With input resistors bypassed. Bypassing resistors will reduce the lifetime of relays. See the rated load relay lif characteristics. |  |  |  |  |  |
| 6. Limited to 6 W of channel resistance power loss per module |  |  |  |  |  |
| 7. Speeds are for 2 -wire ohms or DCV, $41 / 2$ digits, delay 0 , display off, autozero off, and within the bank <br> 8. DC or peak AC current |  |  |  |  |  |
| 9 9. Ambient temperature $<30^{\circ} \mathrm{C}$ |  |  |  |  |  |
| 10. Includes $0.5^{\circ} \mathrm{C}$ temperature reference sensor and $0.5^{\circ} \mathrm{C}$ terminal block isothermal gradient error, measured under worst-case loading of the mainframe; see User's Guide for information on supported external reference sensors |  |  |  |  |  |
| 12. Channel resistance is typically < $1.5 \Omega$ but can go as high as $50 \Omega$ when a channel is used in measurement applications with < 10 mA load current. Increased relay channel resistance for measurements with load currents below 10 mA can occur on cards that have been out of service or following relay inactivity for periods of greater than 1 week. Switching relays for 2 K cycles prior to use may reduce the variation in channel resistance. Applies to the 34921A and 34922A. Keysight recommends the use of 4 -wire Ohms for resistance measurements. For high accuracy voltage measurements, select the DMM input resistance setting of >10 G ohms to minimize the impact of relay contact resistance. |  |  |  |  |  |
| 13. Pollution Degree $1 \pm 300$ Vrms or VDC; Pollution Degree $2 \pm 100 \mathrm{~V}$ rms or VDC 14. Pollution Degree $1 \pm 150$ Vpeak; Pollution Degree $2 \pm 100$ Vpeak |  |  |  |  |  |

## 34980A Matrix Switch Modules

The 34980A matrix modules are full Crosspoint matrices that allow you to connect any row to any column. This is a convenient way to connect multiple test instruments to multiple points on a device under test.

Choose from the following features:

- Latching armature relays-Max 300 Vrms or DC, 1 A
- High-speed reed relays-Max 150 Vpeak, 0.5 A
- Configurable dual $4 \times 8$, dual $4 \times 16$, or quad $4 \times 32$ modules
- Single-wire configuration (34933A or 34934A)
- High-density matrix with automatic surge protection and row disconnects for flexible measurements (34934A)
- Analog bus expandable rows to create larger matrices
- (34931A, 32A, 33A)
- Connections via standard 50 or 78-pin D-sub cables or detachable terminal block

Each cross-point in the matrix switch has two wires-a high and a low for the measurement. Or, if you prefer, the 34933A and 34934A can be configured as a single-wire matrix, increasing the number of channels.


Figure 5. 34932A dual $4 \times 16$ armature matrix

The 34933A also has in-rush resistors on each column for added protection. The 34934A also has in-rush protection resistors but also has an automatic bypass switch for flexibility in making low-level measurements. Row disconnect switches also reduce the capacitance loading when combining modules to create larger matrices.

Multiple matrix modules can be combined through the analog bus or the row expansion kit (34934A only) to create a larger matrix. The matrix can then be connected to the internal DMM for easy measurements.

Combine your matrix with a multiplexer switch to achieve the desired switching topology and get a lowercost solution with better specifications. All the matrix switches include a relay counter to help predict when relays need to be replaced. Use the sequencing feature to easily change between different cross-point setups.

NOTE: The 34933A and 34934A have 100-ohm input protection resistors to limit current and protect the reed relays.


Figure 6. 34934A quad $4 \times 32$ matrix (1 of 4 matrices shown)


## 34980A General-Purpose Switch Modules

The 34980A general-purpose switches can be used to route signals or to control other system devices. These switches are ideal for device actuation and switching loads or power supplies.

Choose from the following features:

- Form C channels up to $1 \mathrm{~A}, 60 \mathrm{VA}$
- Form A channels up to 5 A, 150 VA
- Armature latching relays
- Simultaneous channel switching
- Temperature sensor to detect overheating conditions
- Connections via standard 50 or 78-pin Dsub cables or detachable terminal block

The 34937A is the most versatile general-purpose switch with 28 Form C channels that can switch up to 1 A of current. In addition, this module has four Form A channels that can switch up to 5 A of current. For power switching applications, the 34938A has 205 -amp channels in a Form A topology. Each Form A general-purpose switch can handle up to 150 W , enough for many power line switching applications. For high-density applications, the 34939A offers 64 Form A channels for switching up to 1A and carrying currents up to 2A.

The general purpose switches contain latching armature relays where multiple channels can be closed at the same time. Additionally, for switching reactive loads, the optional terminal blocks have pads for snubbing circuits.

The built-in relay counter helps predict when relays need to be replaced.


Figure 7. 34937A 32-channel Form A / Form C


## 34980A RF and Microwave Switch Modules

The 34980A offers a variety of RF and microwave switch modules-RF multiplexers, SPDT switching from DC to 26.5 GHz , or a switch/ attenuator driver module that allows you to control switches or attenuators external to the 34980A mainframe.

## 34941A/42A-from DC to 3 GHz

The RF switch modules can be used to switch signals from DC to 3 GHz and above. This can be useful for switching signals between oscilloscopes, spectrum analyzers, network analyzers, and other RF test equipment.

Choose from the following features:

- 50 - or 75 -ohm Quad 4-channel multiplexers
- DC to 3 GHz
- $30 \mathrm{~V}, 0.5 \mathrm{~A}, 10 \mathrm{~W}$

The 34941A and 34942A are configured as four independent $1 \times 4$ RF multiplexers on a single module. Multiple banks can be connected together to create a larger multiplexer. To prevent ground loops, individual multiplexers are isolated from each other and from the mainframe's chassis. However, the multiplexer channels can be chassis grounded with a simple change. Both 50 -ohm and 75 -ohm versions are available.


Figure 8. 34941A Quad $1 \times 450$ ohm 3 GHz multiplexer


Figure 9. 34941A typical initial crosstalk


Figure 11. 34941A typical initial VSWR


Figure 10. 34941A typical initial insertion loss


Figure 12. 34942A typical initial crosstalk

## 34946A/47A—From DC to 26.5 GHz

For applications where you need only a few high-frequency switches, the 34946A and 34947A offer inglepole, double-throw switches in either $4 \mathrm{GHz}, 20 \mathrm{GHz}$ or 26.5 GHz options. These modules internally mount two or three independent Keysight N1810 series coaxial switches. These switches are well known for their excellent insertion loss, isolation, and VSWR specifications.

Switch read-back capabilities allow you to query the position of the switch. You can choose higher density with the unterminated switches or select the terminated switches to maintain an impedance match.

## 34946A/47A Option 001

These modules can also be ordered without switches installed. This gives you the capability to install your own N1810 series switches or use the module to control the N1810 Series switches outside the mainframe.


Figure 13. 34942A typical initial VSWR


Figure 14. 34942 typical initial insertion loss

N1810 minimum required switch options

| Coil Voltage |
| :--- |
| DC Connector |
| Drive |

Option

| option 124 | 24 VDC coil |
| :---: | :---: |
| option 201 | D" subminiature 9 pin female |
| option 402 | Position indicators |



Figure 15. 34946A Dual 1x2 SPDT terminated microwave switch

DC to 3 GHz
DC to $26.5 \mathrm{GHz}^{3}$

|  | 34941A | 34942A | 34946A | 34947A |
| :---: | :---: | :---: | :---: | :---: |
| Channels | quad $1 \times 4$ | quad $1 \times 4$ | 2 SPDT | 3 SPDT |
| Switch Type | $50 \Omega$ unterminated, latching relays | $75 \Omega$ unterminated, latching relays | $50 \Omega$ terminated | $50 \Omega$ unterminated |
| RF characteristics |  |  |  |  |
| Frequency range ${ }^{2}$ | DC to 3 GHz | DC to 1.5 GHz | DC to 4 GHz , <br> 20 GHz or 26.5 GHz | DC to 4 GHz , <br> 20 GHz or 26.5 GHz |
| $\begin{aligned} & \text { Insertion loss } \left.{ }^{2}<40 \mathrm{C} / 80 \% \mathrm{RH}\right) \\ & 100 \mathrm{MHz} \\ & 1 \mathrm{GHz} \\ & 3 \mathrm{GHz} \end{aligned}$ | 0.15 dB <br> 0.60 dB <br> 1.40 dB | 0.15 dB <br> 0.60 dB <br> N/A | DC to $4 \mathrm{GHz}<0.42 \mathrm{~dB}$, <br> @ $20 \mathrm{GHz}<0.69 \mathrm{~dB}$, <br> @ $26.5 \mathrm{GHz}<0.8 \mathrm{~dB}$ | DC to $4 \mathrm{GHz}<0.42 \mathrm{~dB}$, <br> @ $20 \mathrm{GHz}<0.69 \mathrm{~dB}$, <br> @ $26.5 \mathrm{GHz}<0.8 \mathrm{~dB}$ |
| VSWR <br> 100 MHz <br> 1 GHz <br> 3 GHz | $\begin{aligned} & 1.03 \\ & 1.25 \\ & 1.55 \end{aligned}$ | $\begin{aligned} & 1.15 \\ & 1.35 \\ & \text { N/A } \end{aligned}$ | DC to $4 \mathrm{GHz}<1.15$, <br> @ $20 \mathrm{GHz}<1.30$, <br> @ $26.5 \mathrm{GHz}<1.6$ | DC to $4 \mathrm{GHz}<1.15$, <br> @ $20 \mathrm{GHz}<1.30$, <br> @ 26.5GHz < 1.6 |
| $\begin{aligned} & \text { Isolation }(\mathrm{dB})^{2} \\ & 100 \mathrm{MHz} \\ & 1 \mathrm{GHz} \\ & 3 \mathrm{GHz} \end{aligned}$ | Contact factory 80 dB <br> 58 dB <br> 40 dB | Contact factory <br> 80 dB <br> 60 dB <br> N/A | DC to $4 \mathrm{GHz}>85 \mathrm{~dB}$, <br> @ $20 \mathrm{GHz}>67 \mathrm{~dB}$, <br> @ $26.5 \mathrm{GHz}>60 \mathrm{~dB}$ | $\begin{aligned} & \text { DC to } 4 \mathrm{GHz}>85 \mathrm{~dB}, @ \\ & 20 \mathrm{GHz}>67 \mathrm{~dB} \text {, } \\ & @ 26.5 \mathrm{GHz}>60 \mathrm{~dB} \end{aligned}$ |
| Spurious noise below 1.3 GHz | $-140 \mathrm{dBm}$ | $-140 \mathrm{dBm}$ | 80 dB | 80 dB |
| Rise time | $<80$ ps | < 160 ps | N/A | N/A |
| Signal delay | <1 ns | $<1 \mathrm{~ns}$ | N/A | N/A |
| Capacitance | $<30 \mathrm{pF}$ | $<30 \mathrm{pF}$ | N/A | N/A |
| Switching characteristics |  |  |  |  |
| Max volts ${ }^{1}$ | 30 V | 30 V | 7 VDC | 7 VDC |
| Max current | 0.5 A | 0.5 A | N/A | N/A |
| Max power | $10 \mathrm{~W}{ }^{5}$ | $10 \mathrm{~W}^{5}$ | 1 W@7 VDC,50 W peak ${ }^{4}$ | 1 W@7 VDC,50 W peak ${ }^{4}$ |
| Offset voltage | $10 \mu \mathrm{~V}$ | $10 \mu \mathrm{~V}$ | N/A | N/A |
| Initial channel resistance ${ }^{6}$ | $1 \Omega$ | $1 \Omega$ | N/A | N/A |
| Volt-Hertz limit | $2 \times 10^{10}$ | $2 \times 10^{10}$ |  |  |
| General characteristics |  |  |  |  |
| Relay life | 300,000 at $30 \mathrm{~V} / 10 \mathrm{~mA}$ load; 100,000 at 10 W load RF SAmeas | 300,000 at $30 \mathrm{~V} / 10 \mathrm{~mA}$ <br> load; 100,000 at 10 W load RF SAmeas | > 5 M cycles | > 5 M cycles |
| Open/close time | $18 \mathrm{~ms} / 18 \mathrm{~ms}$ | $18 \mathrm{~ms} / 18 \mathrm{~ms}$ | $<15 \mathrm{~ms} / 15 \mathrm{~ms}$ | $<15 \mathrm{~ms} / 15 \mathrm{~ms}$ |
| Connector type | Female SMA | Mini $75 \Omega$ SMB | Female SMA | Female SMA |
| Coil voltage | N/A | N/A | 24 VDC | 24 VDC |
| Analog bus backplane connection | No | No | No | No |
| Note: <br> 1. Channel-to-earth <br> 2. $50 \Omega$ source, 50 <br> 3. For more detaile for the 34947A. <br> 4. 10 usec maximu <br> 5. Max power is 1 <br> 6. Channel resistan applications with below 10 mA can than 1 week. Sw the 34941A and | load (75 $\Omega$ for 34942 switch specifications, <br> duration between 30 MHz and is typically $<1.5 \Omega$ 10 mA load current. ccur on cards that hav hing relays for 2 K cy 942A. | e the Keysight N1810TL <br> GHz for CISPR 11 comp can go as high as $50 \Omega$ reased relay channel re been out of service or s prior to use may redu | N1811TL, N1812UL for <br> ance <br> hen a channel is used stance for measureme lowing relay inactivity the variation in channe | 946A, and N1810UL <br> measurement with load currents periods of greater sistance. Applies to |

## 34945A/34945EXT Microwave Switch/Attenuator Driver

This module allows you to control switches, attenuators, and other devices external to the 34980A. The 34945A / 34945EXT provides the power and control signals for many of the most popular microwave switches and attenuators. One 34945A /34945EXT combination can drive up to 64 switch coils-that's 32 standard SPDT switches. The 34945A /EXT can be extended by adding additional 34945EXT boards. The first 34945EXT is powered by the mainframe. You can add up to seven additional 34945EXT boards with user-supplied power. Multiple switch operations are performed in sequential order, or for faster, simultaneous switching, you can connect an external power supply to the 34945EXT.

The Y1150A-Y1155A distribution boards enable simple connections to the external switches. The distribution boards plug onto the 34945EXT and are used to route the power and control signals from the driver module to the switches using standard cables.

The 34945A /34945EXT also has sensing capabilities that allow a read back of the actual position of the switch or attenuator. Drive signals for LED indicators are also provided to give a visual indication of the switch position. The following microwave switches and attenuators are supported by the Y1150AY1155A distribution boards:

- N181x/U9397x series SPDT switches
- $8762 / 3 / 4$ series SPDT switches (screw terminals)
- $8765 x$ coaxial switches
- 8766x/8767x/8768x multiport switches
- 87104x/106x/L710xx/L720xx multiport switches
- 87406x series matrix switches
- $87204 x / 206 x$ series multiport switches
- 87606x series matrix switches
- $87222 x / L 7222$ transfer switches
- 849x and 8490x series attenuators
- Other switches and devices through individual screw terminal connections


Figure 16. 34945A/34945EXT

## 34945EXT switch drive ( 64 channels, low side drive mode)

| General specifications |  |
| :---: | :---: |
| Driver off voltage (max) | 30 V |
| Driver off leakage current | $500 \mu \mathrm{~A}$ |
| Driver on current (max) | 600 mA |
| Driver on voltage (max) | $0.5 \mathrm{~V} @ 600 \mathrm{~mA}$ |
| 34945EXT switch drive (64 channels, TTL drive mode) |  |
| Hi output voltage | 3 V @ lout = 2 mA |
| Lo output voltage | 0.4 V @ lin $=20 \mathrm{~mA}$ |
| Lo input Current | 20 mA |
| 34945EXT position indicator sense inputs |  |
| Channels | 64 |
| Lo input voltage (max) | 0.8 V |
| Hi input voltage (min) | 2.5 V |
| Input resistance | $\begin{aligned} & >100 \mathrm{k} \Omega @ \operatorname{Vin} \leq 5 \mathrm{~V} \\ & >20 \mathrm{k} \Omega @ \operatorname{Vin}>5 \mathrm{~V} \\ & \hline \end{aligned}$ |
| Maximum input voltage | 30 V |
| 34945EXT switch drive power supply (34945EXT powered by 34945A) |  |
| Voltage | 24 V nominal (external power supply required for switches needing more than 24 V ) |
| Current | 100 mA continuous + 200 mA ( 15 msec pulse, 25\% duty cycle) |
| 34945EXT external power connection |  |
| Voltage range | 4.75 V to 30 V |
| Current limit | 2 A |
| LED indicator (Current mode divers) |  |
| Channels | 64 |
| Supply voltage | 5 V nominal |
| LED drive current | 5 mA nominal, (prog 1-20 mA) |
| Driver compliance voltage | 0.8 V |
| 34945EXT dimensions |  |
| Dimensions | 11.2 " 4.5 " $\times 1.5$ " high with distribution boards installed |

## 34980A System Control Modules 34950A 64-bit digital I/O with memory and counter

This module can be used to simulate or detect digital patterns. It has eight 8-bit digital I/O channels with handshaking, pattern memory, two 10 MHz counters with gate functions, and programmable clock output.

## Digital input/output

The digital I/O bits are organized into two banks of 32-bits. The I/O bits can be configured and programmed as inputs or outputs in 8-bit channels. The digital outputs can be configured as active drive or open-drain outputs with a $10 \mathrm{k} \Omega$ pull up. User-supplied pull-up resistors for up to 5 V outputs are also acceptable. The digital inputs have programmable thresholds up to 5 V for compatibility with most digital logic standards.

The onboard pattern memory can be used to select and output digital stimulus or bit-stream patterns or to capture external digital data. Each bank has independent memory and directional control so that one bank can output data while the other captures data. The memory can be divided up to 64 Kbytes per 8-bit channel.

Specifically, the digital I/O channels also have:

- Variable active high drive output from 1.65 V to 5 V or open-drain
- Variable input thresholds from 0 V to 5 V
- Configurable handshaking protocols, including synchronous and strobe
- Programmable polarity
- Source or sink up to 24 mA with an Imax of 400 mA per module.
- Internal alarming for maskable pattern match
- 1 hardware pattern interrupt per bank
- Connections via standard 78-pin Dsub cables or detachable terminal block

Frequency counter/totalizer
The two channels can be used to count digital events, frequency, period, duty cycle, totalize, and pulse width. The counter/totalizer also includes:

- Programmable gate functionality
- Programmable input thresholds levels 0 V to 3 V

34950A 64 bit I/O

## Digital input/output characteristics

| Number of channels | Eight 8 bits. Input or output, non-isolated |
| :---: | :---: |
| $V_{\text {in }}$ | $0 \mathrm{~V}-5 \mathrm{~V}^{1}$ |
| $V_{\text {out }}$ | $1.65 \mathrm{~V}-5 \mathrm{~V}^{1,2}$ |
| lout | (max) $24 \mathrm{~mA}^{2}$ |
| Frequency (max) | $10 \mathrm{MHz}{ }^{3}$ |
| load (max) | 400 mA |
| trise + taall (typ) | $6 \mathrm{~ns}^{5}$ |

## Handshake lines

| $V$ in | $0 \mathrm{~V}-5 \mathrm{~V}$ |
| :---: | :---: |
| Vout | $1.65 \mathrm{~V}-5 \mathrm{~V}^{2,4}$ |
| Iout (max) | $24 \mathrm{~mA}^{2}$ |
| Frequency (max) | 10 MHz |
| Counter function characteristics |  |
| Max frequency | 10 MHz (max) 50\% duty cycle |
| $\mathrm{V}_{\text {in }}$ | 0 V to 5 V |
| Min rise/fall time | $5 \mu \mathrm{sec}$ |

## Totalizer function characteristics

| Maximum count | $2^{32}-1(4,294,967,296)$ |
| :---: | :---: |
| Maximum input frequency | $10 \mathrm{MHz}(\mathrm{max})$, rising or falling edge, programmable |
| $V_{\text {in }}$ | $0 \mathrm{~V}-5 \mathrm{~V}$ |
| Gate input | $0 \mathrm{~V}-5 \mathrm{~V}$ |
| Maximum input frequency | $5 \mu \mathrm{sec}$ |

System clock generator characteristics

| Frequency | $20 \mathrm{MHz}-10 \mathrm{~Hz}$, configurable divide by-n 24-bits, programmable on/off |
| :---: | :---: |
| $\checkmark$ out | $1.65 \mathrm{~V}-5 \mathrm{~V}^{2}$ |
| Iout (max) | $24 \mathrm{~mA}^{2}$ |
| Accuracy | 100 ppm |

Note:

1. Configurable by 8-bit channel
2. Lower current drive at lower voltages
3. From memory with handshaking
4. Configurable by bank
5. $5 \mathrm{~V}, 50 \mathrm{pF}$ load


Figure 17. 34950A 64-channel digital I/O

## 34951A 4-Channel Isolated DIA Converter with Waveform Memory

This module has four independent, isolated channels that output DC voltage up to $\pm 16 \mathrm{~V}$ or DC current up to $\pm 20 \mathrm{~mA}$. The gain and offset can be adjusted on-the-fly.

Each channel can be controlled manually or use the onboard memory to download a waveform. The 500k of memory is global and can store up to 32 waveforms. Any waveform can be dynamically allocated among one or more channels and output as a point-to-point arbitrary waveform generator at up to 200k points/sec. You can use the standard since square or ramp wave shapes provided or define your own wave shape using over 500k points and output to a device under test. There is also a single CLK that can be divided down for each channel independently. The calibration command connects the D/A converters to the internal DMM to be automatically calibrated. Connections to the module can be made via standard 50-pin Dsub, cables, or a detachable terminal block.

34951A 4 channel D/A

| General specifications |  |
| :---: | :---: |
| Maximum update rate | 200 kHz point-to-point |
| Monotonic | To 16 bits |
| Isolation | > 80 VDC/AC peak (chan-to-chassis or chan-to-chan) |
| Synchronization | Software commands or external trigger |
| Internal/external CLK accuracy | 100 ppm |
| AC accuracy | Not specified |
| DC voltage |  |
| Amplitude | $\pm 16 \mathrm{~V}$ up to 10 mA |
| Resolution | 16 bits $=500 \mu \mathrm{~V}$ |
| Amplitude accuracy | $\pm$ ( $0.05 \%+3.0 \mathrm{mV}$ ) (90 days, $\mathrm{Tcal} \pm 5^{\circ} \mathrm{C}$ or Cal:MOD?: $\pm 5^{\circ} \mathrm{C}$ ) |
| Ripple and noise | $<2 \mathrm{mVrms}, 20 \mathrm{~Hz}$ to 250 kHz into $10 \mathrm{k} \Omega$ load |
| Settling time | $40 \mu \mathrm{~S}$ (-full scale to +full scale step, single-channel, to rated accuracy) |
| Output impedance | $<1 \Omega$ with the load sensed |
| DC current |  |
| Range | $\pm 20 \mathrm{~mA}$ |
| Resolution | 16 -bit $=630 \mathrm{nA}$ |
| Accuracy | $\pm(0.09 \%+5.0 \mu \mathrm{~A})\left(90\right.$ days, $\mathrm{Tcal} \pm 5^{\circ} \mathrm{C}$ or Cal:MOD?: $\left.\pm 5^{\circ} \mathrm{C}\right)$ |
| Ripple and noise | $<2 \mu \mathrm{Arms}, 20 \mathrm{~Hz}$ to 250 kHz into $250 \Omega$ |
| Compliance voltage | $\pm 12 \mathrm{~V}$ |
| Maximum open circuit voltage | < $\pm 22 \mathrm{~V}$ |
| Trigger input |  |
| Input level | TTL compatible (3.3 V logic, 5 V tolerant) |
| Slope | Rising or falling, selectable |
| Pulse width | > 100 ns |
| Input impedance | $>10 \mathrm{k} \Omega$, DC coupled |


| Trigger output |  |
| :---: | :---: |
| Level | TTL compatible into $1 \mathrm{k} \Omega$ (3.3 V logic) |
| Output impedance | $50 \Omega$ typical |
| Clock input |  |
| Input level | TTL compatible into $1 \mathrm{k} \Omega$ (3.3 V logic) |
| Input impedance | $50 \Omega$ typical |
| Maximum rate | 10 MHz |
| Clock output |  |
| Level | TTL compatible into $1 \mathrm{k} \Omega$ (3.3 V logic) |
| Output impedance | $50 \Omega$ typical |
| Maximum rate | 10 MHz |
| Accuracy | $\pm 100 \mathrm{ppm}$ |

## 34952A Multifunction Module with 32-bit DIO, 2-Channel DIA and Totalizer

The multifunction module offers the flexibility you need for system control. The 34952A has four 8-bit digital I/O channels, a 100 kHz gated totalizer, and two $\pm 12 \mathrm{~V}$ analog outputs-all on a single earthreferenced module. The digital inputs and totalizer input may be included in a scan list. Alarm limits for the digital and totalizer inputs are evaluated continuously, capturing and logging alarm conditions even between scans. Connections can be made via standard 50 -pin Dsub cables or detachable terminal blocks. The 34952T terminal block has a pinout for connection to an external Opto 22 board.

## 34952A multifunction module

## Digital input/output characteristics

| Channels | Four 8-bit channels, 8 bits wide, input or output, non-isolated |
| :---: | :---: |
| Vin(L) | $<0.8 \mathrm{~V}$ (TTL) |
| $\operatorname{Vin}(\mathrm{H})$ | > 2.0 V (TTL) |
| Vout(L) | < 0.8 V @ lout $=-400 \mathrm{~mA}$ per output |
| Vout(H) | > 2.4 V @ lout = 1 mA |
| Vin(H) max | < 42 VDC with external open drain pull-up |
| Alarm | Maskable pattern match or state change |
| Speed | 4 ms (max) alarm sampling |
| Latency | 5 ms (typical) to 34980A alarm output |
| Read/write speed | 95/s |
| Totalize input characteristics |  |
| Max count | $2^{26}-1$ |
| Totalize input | 100 kHz (max) rising or falling edge, programmable |
| Signal level | $1 \mathrm{Vp}-\mathrm{p}$ (min) 42 Vpk (max) |
| Threshold | 0 V or TTL |
| Gate input | TTL-Hi, TTL-Lo, or none |
| Count reset | Manual or read + reset |
| Read speed | $85 \mathrm{rds} / \mathrm{s}$ |
| Analog output characteristics |  |
| DAC 1,2 | $\pm 12 \mathrm{~V}$, non-isolated |
| Resolution | 1 mV |
| IOUT | 10 mA max |
| Settling time | 1 ms to $0.01 \%$ of output |
| Accuracy <br> 1 year | $\begin{aligned} & \pm(\% \text { of output }+\mathrm{mV}) \\ & (0.25 \%+20 \mathrm{mV}) \end{aligned}$ |
| Temp. coefficient | $\pm(0.015 \%+1 \mathrm{mV}) /^{\circ} \mathrm{C}$ |



Figure 18. 34952A multifunction module

## 34959A Breadboard Component Module

Use this module to create your own custom designs inside the 34980A mainframe. You can control your custom circuits with access to both the +12 V and +5 V supplies, 28 relay drive lines, and two 8-bit GPIO ports. Your design can be isolated from the analog buses or connected by loading the backplane switches. Simply mount your custom PC board or other components into the space provided and connect via the two ribbon connectors provided. The module is provided with two 50- or 78-pin Dsub connector openings. For custom connections, use the detachable flat faceplates for easy modification. You can program your circuitry using standard read and write commands in SCPI.

34959A breadboard component module

| General specifications |  |
| :---: | :---: |
| Max module power dissipation | 6 W of channel resistance |
| Power available: |  |
| 12 V regulation no load to full load | 10\% |
| 5 V regulation no load to full load | 5\% |
| Max power from 12 V | 6 W |
| Max power from 5V | 1 W |
| Relay drives |  |
| Channels | 28, sink up to 100 mA |
| Max Input Voltage | 42 VDC |
| Leakage Current | $8 \mu \mathrm{~A}$ |
| GPIO ports |  |
| Chan 1 and Chan 2 | 8 configure bits as input or output |
| Chan 3 | 3 output bits |
| High input | 2 V min, 5.5 V max |
| Low input | 0 V min, 0.8 V max |
| High output | 2.4 V @ $4 \mathrm{~mA}, 3 \mathrm{~V}$ @ $500 \mu \mathrm{~A}$ |
| Low output | 0.4 V @ 8 mA |

## 34959A breadboard component module conditions of acceptability:

1. Follow local laws and regulations provided by the Authority Having Jurisdictions (AHJ). This equipment is Certified as a component for use in other CSA Certified equipment where the suitability of the combination is to be determined in the end use application.
2. When interconnecting the 34959A component module to the system components during installation, the overall 34980A and all installed modules for the system and their maximum rated allowable inputs default to the lowest rating of any one system component or module.
3. With *hazardous voltages anywhere in the overall 34980A and all installed modules use only female D-Sub output connectors Keysight Part Number DB-50(F) 1253-5854 or DB-78(F) 1253-5007.
4. With only *non-hazardous voltages in the overall 34980A and all installed modules use either female D-Sub output connectors Keysight Part Number DB-50(F) 1253-5854 or DB-78(F) 12535007, or male D-Sub output connectors Keysight Part Number DB-50(M) 1253-5853 or DB-78(M) 1253-6006.
5. Install overcurrent protection with devices rated or the snubber circuits to limit the current according to the 34959A Current Limiting Graph located in the User's Guide.
6. Install only Keysight supplied relays 0490-1954 when connecting to the Analog Bus.
*Any voltages greater than 30Vrms, 42.4 Vpeak and 60Vdc are considered hazardous (IEC 61010-1).

Available space for internal board/components: $5.4 \times 7.5 \times$ either 0.9 inches in height without PC board or 0.7 inches high with a PC board.


Figure 19. 34959A breadboard component module

## 34980A System Specifications and Characteristics

DMM accuracy $\pm$ (\% of reading + \% of range)
Includes measurement error, switching error, and transducer conversion error ${ }^{1}$.

| Range ${ }^{4}$, Frequency, etc. | $\begin{aligned} & 24 \text { hour } 2,3 \\ & \text { Tcal } \pm 1^{\circ} \mathrm{C} \end{aligned}$ | 90 days <br> Tcal $\pm 5^{\circ} \mathrm{C}$ | 1 year Tcal $\pm 5^{\circ} \mathrm{C}$ | Temperature coefficient $/{ }^{\circ} \mathrm{C}>$ Tcal $\pm 5^{\circ} \mathrm{C}$ |
| :---: | :---: | :---: | :---: | :---: |
| DC voltage (with 34921A / 22A / 31A / 32A) ${ }^{10,11} \mid$ Input impedance $=\mathrm{Hi}-\mathrm{Z} \mid 10 \mathrm{~V}$ range and below |  |  |  |  |
| $\begin{aligned} & 100.0000 \mathrm{mV} \\ & 1.000000 \mathrm{~V} \\ & 10.00000 \mathrm{~V} \\ & 100.0000 \mathrm{~V} \\ & 300.0000 \mathrm{~V} \end{aligned}$ | $\begin{aligned} & 0.0030+0.0035 \\ & 0.0020+0.0006 \\ & 0.0015+0.0004 \\ & 0.003+0.0006 \\ & 0.003+0.0020 \end{aligned}$ | $\begin{aligned} & 0.0040+0.0040 \\ & 0.0030+0.0007 \\ & 0.0020+0.0005 \\ & 0.0045+0.0006 \\ & 0.0045+0.0030 \end{aligned}$ | $\begin{aligned} & 0.0050+0.0040 \\ & 0.0040+0.0007 \\ & 0.0035+0.0005 \\ & 0.0055+0.0006 \\ & 0.0055+0.0030 \end{aligned}$ | $\begin{aligned} & 0.0005+0.0005 \\ & 0.0005+0.0001 \\ & 0.0005+0.0001 \\ & 0.0005+0.0001 \\ & 0.0005+0.0003 \end{aligned}$ |
| True RMS AC voltage ${ }^{5} \mid$ All ranges from 100.0000 mV to 100.0000 V |  |  |  |  |
| $3 \mathrm{~Hz}-5 \mathrm{~Hz}$ <br> $5 \mathrm{~Hz}-10 \mathrm{~Hz}$ <br> $10 \mathrm{~Hz}-20 \mathrm{kHz}$ <br> $20 \mathrm{kHz}-50 \mathrm{kHz}$ <br> $50 \mathrm{kHz}-100 \mathrm{kHz}$ <br> $100 \mathrm{kHz}-300 \mathrm{kHz}{ }^{6}$ | $\begin{aligned} & 1.00+0.03 \\ & 0.35+0.03 \\ & 0.04+0.03 \\ & 0.10+0.05 \\ & 0.55+0.08 \\ & 4.00+0.50 \end{aligned}$ | $\begin{aligned} & 1.00+0.04 \\ & 0.35+0.04 \\ & 0.05+0.04 \\ & 0.11+0.05 \\ & 0.60+0.08 \\ & 4.00+0.50 \end{aligned}$ | $\begin{aligned} & 1.00+0.04 \\ & 0.35+0.04 \\ & 0.06+0.04 \\ & 0.12+0.05 \\ & 0.60+0.08 \\ & 4.00+0.50 \end{aligned}$ | $\begin{aligned} & 0.100+0.004 \\ & 0.035+0.004 \\ & 0.005+0.004 \\ & 0.011+0.005 \\ & 0.060+0.008 \\ & 0.20+0.02 \end{aligned}$ |
| True RMS AC voltage ${ }^{5} \mid 300.0000 \mathrm{~V}$ range |  |  |  |  |
| $3 \mathrm{~Hz}-5 \mathrm{~Hz}$ <br> $5 \mathrm{~Hz}-10 \mathrm{~Hz}$ <br> $10 \mathrm{~Hz}-20 \mathrm{kHz}$ <br> $20 \mathrm{kHz}-50 \mathrm{kHz}$ <br> $50 \mathrm{kHz}-100 \mathrm{kHz}$ <br> $100 \mathrm{kHz}-300 \mathrm{kHz}{ }^{6}$ | $\begin{aligned} & 1.00+0.05 \\ & 0.35+0.05 \\ & 0.04+0.05 \\ & 0.10+0.10 \\ & 0.55+0.20 \\ & 4.00+1.25 \end{aligned}$ | $\begin{aligned} & 1.00+0.08 \\ & 0.35+0.08 \\ & 0.05+0.08 \\ & 0.11+0.12 \\ & 0.60+0.20 \\ & 4.00+1.25 \end{aligned}$ | $\begin{aligned} & 1.00+0.08 \\ & 0.35+0.08 \\ & 0.06+0.08 \\ & 0.12+0.12 \\ & 0.60+0.20 \\ & 4.00+1.25 \end{aligned}$ | $\begin{aligned} & 0.100+0.008 \\ & 0.035+0.008 \\ & 0.005+0.008 \\ & 0.011+0.012 \\ & 0.060+0.020 \\ & 0.20+0.05 \end{aligned}$ |
| Resistance ${ }^{7}$ |  |  |  |  |
| $\begin{aligned} & 100.0000 \Omega, 1 \mathrm{~mA} \\ & 1.000000 \mathrm{k} \Omega, 1 \mathrm{~mA} \\ & 10.00000 \mathrm{k} \Omega, 100 \mu \mathrm{~A} \\ & 100.0000 \mathrm{k} \Omega, 10 \mu \mathrm{~A} \\ & 1.000000 \mathrm{M} \Omega, 5.0 \mu \mathrm{~A} \\ & 10.00000 \mathrm{M} \Omega, 500 \mathrm{nA} \\ & 100.0000 \mathrm{M} \Omega, 500 \mathrm{nA} / 10 \mathrm{M} \Omega \end{aligned}$ | $\begin{aligned} & 0.0030+0.0035 \\ & 0.0020+0.0006 \\ & 0.0020+0.0005 \\ & 0.0020+0.0005 \\ & 0.002+0.001 \\ & 0.015+0.001 \\ & 0.300+0.010 \end{aligned}$ | $\begin{aligned} & 0.008+0.004 \\ & 0.008+0.001 \\ & 0.008+0.001 \\ & 0.008+0.001 \\ & 0.008+0.001 \\ & 0.020+0.001 \\ & 0.800+0.010 \end{aligned}$ | $\begin{aligned} & 0.010+0.004 \\ & 0.010+0.001 \\ & 0.010+0.001 \\ & 0.010+0.001 \\ & 0.010+0.001 \\ & 0.040+0.001 \\ & 0.800+0.010 \end{aligned}$ | $\begin{aligned} & 0.0006+0.00050 .0006+ \\ & 0.00010 .0006+0.0001 \\ & 0.0006+0.00010 .0010+ \\ & 0.00020 .0030+0.0004 \\ & 0.1500+0.0002 \end{aligned}$ |
| Frequency and period ${ }^{8}$ |  |  |  |  |
| 100 mV to 300 V range: $\begin{aligned} & 3 \mathrm{~Hz}-5 \mathrm{~Hz} \\ & 5 \mathrm{~Hz}-10 \mathrm{~Hz} \\ & 10 \mathrm{~Hz}-40 \mathrm{~Hz} \\ & 40 \mathrm{~Hz}-300 \mathrm{kHz} \end{aligned}$ | $\begin{aligned} & 0.10 \\ & 0.05 \\ & 0.03 \\ & 0.006 \end{aligned}$ | $\begin{aligned} & 0.10 \\ & 0.05 \\ & 0.03 \\ & 0.01 \end{aligned}$ | $\begin{aligned} & 0.10 \\ & 0.05 \\ & 0.03 \\ & 0.01 \end{aligned}$ | $\begin{aligned} & 0.005 \\ & 0.005 \\ & 0.001 \\ & 0.001 \end{aligned}$ |
| DC current (34921A only) |  |  |  |  |
| $\begin{aligned} & 10.00000 \mathrm{~mA},<0.1 \mathrm{~V}, \text { burden typ } \\ & 100.0000 \mathrm{~mA},<0.6 \mathrm{~V} \\ & 1.000000 \mathrm{~A},<2 \mathrm{~V} \end{aligned}$ | $\begin{aligned} & 0.005+0.0100 .010+ \\ & 0.0040 .050+0.006 \end{aligned}$ | $\begin{aligned} & 0.030+0.0200 .030+ \\ & 0.0050 .080+0.010 \end{aligned}$ | $\begin{aligned} & 0.050+0.0200 .050+ \\ & 0.0050 .100+0.010 \end{aligned}$ | $\begin{aligned} & 0.002+0.00200 .002+ \\ & 0.00050 .005+0.0010 \end{aligned}$ |
| True RMS AC current (34921A only) |  |  |  |  |
| $\begin{aligned} & 10.00000 \mathrm{~mA} \text { and } 51.0 \mathrm{~A} \\ & 3 \mathrm{~Hz}-5 \mathrm{~Hz} \\ & 5 \mathrm{~Hz}-10 \mathrm{~Hz} \\ & 10 \mathrm{~Hz}-5 \mathrm{kHz} \end{aligned}$ | $\begin{aligned} & 1.00+0.04 \\ & 0.30+0.04 \\ & 0.10+0.04 \end{aligned}$ | $\begin{aligned} & 1.00+0.04 \\ & 0.30+0.04 \\ & 0.10+0.04 \end{aligned}$ | $\begin{aligned} & 1.00+0.04 \\ & 0.30+0.04 \\ & 0.10+0.04 \end{aligned}$ | $\begin{aligned} & 0.100+0.006 \\ & 0.035+0.006 \\ & 0.015+0.006 \end{aligned}$ |
| $\begin{aligned} & 100.0000 \mathrm{~mA} 9 \\ & 3 \mathrm{~Hz}-5 \mathrm{~Hz} \\ & 5 \mathrm{~Hz}-10 \mathrm{~Hz} \\ & 10 \mathrm{~Hz}-5 \mathrm{kHz} \end{aligned}$ | $\begin{aligned} & 1.00+0.5 \\ & 0.30+0.5 \\ & 0.10+0.5 \end{aligned}$ | $\begin{aligned} & 1.00+0.5 \\ & 0.30+0.5 \\ & 0.10+0.5 \end{aligned}$ | $\begin{aligned} & 1.00+0.5 \\ & 0.30+0.5 \\ & 0.10+0.5 \end{aligned}$ | $\begin{aligned} & 0.100+0.006 \\ & 0.035+0.006 \\ & 0.015+0.006 \end{aligned}$ |

1. One hour warm-up and a fixed configuration with slow AC filter, sine wave input, and $61 / 2$ digits. Temperature within $\pm$ $5^{\circ} \mathrm{C}$ of temperature at calibration (Tcal between $18-28^{\circ} \mathrm{C}$ )
2. 90 minute warm-up and a fixed configuration and $61 / 2$ digits. Temperature within $\pm 1^{\circ} \mathrm{C}$ of temperature at calibration (Tcal between $18-28^{\circ} \mathrm{C}$ )
3. Relative to calibration standards
4. $20 \%$ over range on all ranges except 300 VDC and $A C$ ranges and 1 ADC and AC current ranges
5. For sine wave input $>5 \%$ of range; for inputs from $1 \%$ to $5 \%$ of range and $<50 \mathrm{kHz}$ add $0.1 \%$ of range additional error; for AC filter slow
6. For sine wave input $>5 \%$ of range, limited to $1 \times 10^{8}$ volt-hertz. Typically, $30 \%$ of reading error at 1 MHz .
7. Accuracy for 4 -wire ohms. Add 4 ohms uncertainty (typical) for 2 -wire ohms function due to conductor and contact resistance variability in the DMM. The 2 -wire resistance accuracy can be improved by measuring a short on each channel and using the built-in $y=M x+b$ scaling function. See User Guide, Chapter 3. The series resistance of the $34923 / 24 / 25 / 33 / 34$ limits the use of the 100 ohms resistance range. Keysight recommends the use of 4 -wire ohms for resistance measurements. For high accuracy voltage measurements, select the DMM input resistance setting of $>10$ G ohms to minimize the impact of relay contact resistance.
8. For inputs $>100 \mathrm{mV}$; for inputs 10 mV to 100 mV multiply $\%$ of reading error $X 10$; for 1 sec aperture (6 1/2 digits)
9. Specified only for inputs $>10 \mathrm{~mA}$. For AC filter slow
10. Add 50 uV error for $34923 \mathrm{~A} / 24 \mathrm{~A} / 33 \mathrm{~A}$ and $7 \mu \mathrm{~V}$ error for the 34925 A
11. Voltage measurements using Fixed-Z input impedance for the 100 V and 300 V ranges.

Additional low frequency error for ACV, ACI (\% of reading)

| Frequency | AC Filter Slow | AC Filter Medium | AC Filter Fast |
| :--- | :--- | :--- | :--- |
| $10 \mathrm{~Hz}-20 \mathrm{~Hz}$ | 0 | 0.74 | - |
| $20 \mathrm{~Hz}-40 \mathrm{~Hz}$ | 0 | 0.22 | - |
| $40 \mathrm{~Hz}-100 \mathrm{~Hz}$ | 0 | 0.06 | 0.73 |
| $100 \mathrm{~Hz}-200 \mathrm{~Hz}$ | 0 | 0.01 | 0.22 |
| $200 \mathrm{~Hz}-1 \mathrm{kHz}$ | 0 | 0 | 0.18 |
| $>1 \mathrm{kHz}$ | 0 | 0 | 0 |

Additional error for frequency, Period (\% of reading)

| Aperture (Digits) |  |  |  |
| :---: | :---: | :---: | :---: |
| Frequency | 1 second ( $61 / 2$ digits) | seconds ( 5112 digits) | 0.01 seconds ( $41 / 2$ digits) |
| $3 \mathrm{~Hz}-5 \mathrm{~Hz}$ | 0 | 0.12 | 0.12 |
| $5 \mathrm{~Hz}-10 \mathrm{~Hz}$ | 0 | 0.17 | 0.17 |
| $10 \mathrm{~Hz}-40 \mathrm{~Hz}$ | 0 | 0.2 | 0.2 |
| $40 \mathrm{~Hz}-100 \mathrm{~Hz}$ | 0 | 0.06 | 0.21 |
| $100 \mathrm{~Hz}-300 \mathrm{~Hz}$ | 0 | 0.03 | 0.21 |
| $300 \mathrm{~Hz}-1 \mathrm{kHz}$ | 0 | 0.01 | 0.07 |
| $>1 \mathrm{kHz}$ | 0 | 0 | 0.02 |

Temperature measurement accuracy

| Temperature 1-year accuracy | Type | Best Range ${ }^{1}$ |  | Extended Range ${ }^{1}$ |  | Temperature coefficient $/{ }^{\circ} \mathrm{C}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Thermocouple (34921A only, includes cold junction accuracy on terminal block) | B | $1100{ }^{\circ} \mathrm{C}$ to $1820^{\circ} \mathrm{C}$ | $1.2{ }^{\circ} \mathrm{C}$ | $400{ }^{\circ} \mathrm{C}$ to $1100{ }^{\circ} \mathrm{C}$ | $1.8{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
|  | E | $-150{ }^{\circ} \mathrm{C}$ to $1000^{\circ} \mathrm{C}$ | $1.0^{\circ} \mathrm{C}$ | $-200{ }^{\circ} \mathrm{C}$ to $-150^{\circ} \mathrm{C}$ | $1.5{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
|  | J | $-150{ }^{\circ} \mathrm{C}$ to $1200^{\circ} \mathrm{C}$ | $1.0{ }^{\circ} \mathrm{C}$ | $-210{ }^{\circ} \mathrm{C}$ to $-150^{\circ} \mathrm{C}$ | $1.2{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
|  | K | $-100{ }^{\circ} \mathrm{C}$ to $1200^{\circ} \mathrm{C}$ | $1.0{ }^{\circ} \mathrm{C}$ | $-200{ }^{\circ} \mathrm{C}$ to $-100^{\circ} \mathrm{C}$ | $1.5{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
|  | N | $-100{ }^{\circ} \mathrm{C}$ to $1300^{\circ} \mathrm{C}$ | $1.0{ }^{\circ} \mathrm{C}$ | $-200{ }^{\circ} \mathrm{C}$ to $-100^{\circ} \mathrm{C}$ | $1.5{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
|  | R | $300{ }^{\circ} \mathrm{C}$ to $1760^{\circ} \mathrm{C}$ | $1.2{ }^{\circ} \mathrm{C}$ | $-50^{\circ} \mathrm{C}$ to $300^{\circ} \mathrm{C}$ | $1.8{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
|  | S | $400^{\circ} \mathrm{C}$ to $1760^{\circ} \mathrm{C}$ | $1.2{ }^{\circ} \mathrm{C}$ | $-50{ }^{\circ} \mathrm{C}$ to $400{ }^{\circ} \mathrm{C}$ | $1.8{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
|  | T | $-100^{\circ} \mathrm{C}$ to $400^{\circ} \mathrm{C}$ | $1.0^{\circ} \mathrm{C}$ | $-200{ }^{\circ} \mathrm{C}$ to $-100^{\circ} \mathrm{C}$ | $1.5{ }^{\circ} \mathrm{C}$ | $0.03{ }^{\circ} \mathrm{C}$ |
| RTD | Ro from $49 \Omega$ to $2.1 \mathrm{k} \Omega$ | $-200{ }^{\circ} \mathrm{C}$ to $600^{\circ} \mathrm{C}$ | $0.06{ }^{\circ} \mathrm{C}$ |  |  | $0.003{ }^{\circ} \mathrm{C}$ |
| Thermistor | 2.2k, 5k, 10k | $-80^{\circ} \mathrm{C}$ to $150{ }^{\circ} \mathrm{C}$ | $0.08{ }^{\circ} \mathrm{C}$ |  |  | $0.002{ }^{\circ} \mathrm{C}$ |

Note:

1. For total measurement accuracy, add temperature probe error, for $<1$ oC accuracy, an external fixed reference is required.

## Typical System Speeds (measurements made on a 3.2 GHz PC running VB6 in Windows XP Pro)

| Single channel reading times in msec |  | Direct measurements - direct to I/O (includes switch, measure time and I/O time) |  |  | Direct measurement to memory(GPIB) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Single-ch | el 1,2 | GPIB <br> msec | USB 2.0 msec | LAN (w/VXI 11) msec | Measurement into memory msec |
| Single ch | nel, DCV | 2.83 | 3.14 | 4.57 | 1.90 |
| Single ch | nel, ACV | 5.00 | 5.35 | 5.75 | 4.00 |
| Single ch | nel, ohms | 2.91 | 3.14 | 4.65 | 1.90 |
| Single ch (eg MEAS | nel while changing scale CV 10 / MEAS DCV 1) | 9.52 | 10.64 | 11.76 | 8.40 |
| Single ch (eg. MEA | nel while changing function ACV / MEAS DCV) | 128 | 120 | 120 | 120 |
| Command execution time ${ }^{3}$ |  |  |  |  |  |
| 34925A | Open or Close | 0.7 | 0.9 | 1.6 |  |
|  | Read? | 2.9 | 3.3 | 4.7 |  |
|  | Close/Read/Open | 4.8 | 5.3 | 6.5 |  |
|  | Init/*WAI | 1.9 | 2.1 | 3 |  |
|  | Close/Init/Open | 3.7 | 4.1 | 4.7 |  |
| 34923A | Open or Close | 0.9 | 1.2 | 1.8 |  |
|  | Read? | 2.9 | 3.3 | 4.7 |  |
|  | Close/Read/Open | 5.3 | 5.8 | 6.5 |  |
|  | Init/*WAI | 1.9 | 2.1 | 3 |  |
|  | Close/Init/Open | 4.2 | 4.7 | 5.2 |  |
| 34921A | Open or Close | 4.7 | 53 | 5.3 |  |
|  | Read? | 2.9 | 3 | 4.7 |  |
|  | Close/Read/Open | 14 | 15 | 15 |  |
|  | Init/*WAI | 1.9 | 2.1 | 3 |  |
|  | Close/Init/Open | 12.4 | 14 | 4 |  |
| 34934A | Close | 1.8 | 2.2 | 3.1 |  |
|  | Read? | 2.8 | 3.1 | 4.8 |  |
|  | Open | 1.5 | 1.8 | 3.2 |  |

Note:
Readings were made with minimum NPLC, delay 0 , display off, autozero off
All times include the issue of "READ?" and the retrieval of data
3. CLOSE or OPEN bus transfer times allowed to overlap previous command; command parse times overlap current activity until IO latency dominant

34934A multi-channel close speeds over GPIB (msec)

|  | Isolate <br> for <br> fixed <br> mode | Auto <br> 100 <br> mode | Auto 0 mode |
| :--- | :--- | :--- | :--- |
| Close 2 | 0.97 | 1.22 | 1.31 |
| channels | 0.43 | 0.54 | 0.56 |
| Close 5 | 0.22 | 0.28 | 0.29 |
| channels | 0.13 | 0.17 | 0.21 |
| Close 10 <br> channels <br> Close 60 <br> channels |  |  |  |

Single channel measurement rates - DMM reading rates ${ }^{1,2}$

| Function | Resolution | Rds/s |
| :---: | :---: | :---: |
| DCV | 4½ digits (0.02 plc) | 3000 |
|  | $51 / 2$ digits ( 1 plc ) | 59 |
|  | $61 / 2$ digits (10 plc) | 6 |
| 2-wire resistance | 4½ digits (0.02 plc) | 2000 |
|  | $51 / 2$ digits (1 plc) | 58 |
|  | $61 / 2$ digits (10 plc) | 6 |
| Thermocouple | (0.02 plc) | 1000 |
|  | $0.1^{\circ} \mathrm{C}$ (1 plc) | 59 |
| RTD/Thermistor | $1^{\circ} \mathrm{C}$ (0.02 plc) | 1900 |
|  | $0.1{ }^{\circ} \mathrm{C}$ (1 plc) | 58 |
|  | $0.01{ }^{\circ} \mathrm{C}$ (10 plc) | 6 |
| ACV | 6½ fast (200Hz) | 350 |
|  | 6112 med ( 20 Hz ) | 350 |
|  | 61/2 slow (3Hz) | 350 |
| Frequency, period | 4½ digits ( 10 ms ) | 70 |
|  | $51 / 2$ digits ( 100 ms ) | 9 |
|  | $61 / 2$ digits (1 s gate) | 1 |
| Note: |  |  |
| 1. Readings speeds for 60 Hz ; autozero off |  |  |

## Scanning measurement rates to bus or memory

|  | Direct measurements - direct to I/O (includes switch, measure time and I/O time) |  |  | Measurement into memory |
| :---: | :---: | :---: | :---: | :---: |
| Scanning channels ${ }^{1}$ | GPIB ch/sec | USB $2.0 \mathrm{ch} / \mathrm{sec}$ | LAN (w VXI 11) ch/sec | Into memory ch/sec |
| Scanning DCV or 2-wire ohms |  |  |  |  |
| 34925A | 920 | 860 | 980 | 1000 |
| 34923A/24A | 588 | 572 | 605 | 625 |
| 34921A/22A | 109 | 109 | 109 | 109 |
| Scanning ACV ${ }^{2}$ |  |  |  |  |
| 34925A | 318 | 315 | 323 | 318 |
| 34923A/24A | 260 | 260 | 260 | 260 |
| 34921A/22A | 88 | 88 | 88 | 88 |
| Scanning temperature     <br> 34921 A 109 109 109 109 |  |  |  |  |
| Scanning digital in 34950A | 660 | 592 | 815 | 1038 |
| Note: <br> 1. Speeds are for $41 / 2$ digit 10 ms for between banks <br> 2. Add additional time for fil | delay 0 , modules $r$ setting | autozero off and measurements; 4 | nning is within bank measurements ar | the same module; add ver |

Data out of memory to LAN, USB, or GPIB (data transfer rate with 1000 channel blocks)

|  | GPIB rds/sec | USB $2.0 \mathrm{rds} / \mathrm{sec}$ | LAN (w VXI 11) ${ }^{1}$ rds/sec |
| :---: | :---: | :---: | :---: |
| Readings | 2560 | 2400 | 3542 |
| Readings with timestamp | 1304 | 1230 | 1826 |
| Readings with all format options ON | 980 | 926 | 1361 |
| Note: <br> 1. LAN large block throughput rat | increased by | 30\% using LAN |  |

## Measurement Characteristics with Optional Internal DMM

## Measurement characteristics

| DC voltage |  |
| :---: | :---: |
| Measurement method | Continuously integrating multi-slope III A-D converter |
| A-D linearity | $0.0002 \%$ of reading $+0.0001 \%$ of range on 10 V range |
| Input resistance <br> $100 \mathrm{mV}, 1 \mathrm{~V}, 10 \mathrm{~V}$ ranges 100 V , <br> 300 V ranges | Selectable $10 \mathrm{M} \Omega$ or $>10,000 \mathrm{M} \Omega 10 \mathrm{M} \Omega \pm 1 \%$ |
| Input bias current | < 50 pA at $25^{\circ} \mathrm{C}$ |
| Input protection | 300 V for Pollution Degree 1 and 100 V for Pollution Degree 2 |
| True RMS AC voltage |  |
| Measurement method | AC coupled True RMS-measures the AC component of the input with up to 300 VDC for Pollution Degree 1 and 100 VDC for Pollution Degree 2 of bias on any range |
| Crest factor | Maximum of 5:1 at full scale |
| Additional crest factor errors (nonsinewave) |  |
| AC filter bandwidth: |  |
| Slow | $3 \mathrm{~Hz}-300 \mathrm{kHz}$ |
| Medium | $20 \mathrm{~Hz}-300 \mathrm{kHz}$ |
| Fast | $200 \mathrm{~Hz}-300 \mathrm{kHz}$ |
| Input impedance | $1 \mathrm{M} \Omega \pm 2 \%$ in parallel with 150 pF |
| Input protection | Pollution Degree 1: 300 Vrms all ranges Pollution Degree 2: 100 Vrms all ranges |
| Resistance |  |
| Measurement method | Selectable 4-wire or 2-wire ohms |
| Current source | Referenced to LO input |
| Offset compensation | Selectable on $100 \Omega, 1 \mathrm{k} \Omega, 10 \mathrm{k} \Omega$ ranges |
| Maximum lead resistance | $10 \%$ of range per lead for $100 \Omega$ and $1 \mathrm{k} \Omega$ ranges. $1 \mathrm{k} \Omega$ on all other ranges |
| Input protection | 300 V for Pollution Degree 1 and 100 for Pollution Degree 2 |
| Frequency and period |  |
| Measurement method | Reciprocal counting technique |
| Voltage ranges | Same as AC voltage function |
| Gate time | $1 \mathrm{~s}, 100 \mathrm{~ms}$, or 10 ms |
| Measurement timeout | Selectable $3 \mathrm{~Hz}, 20 \mathrm{~Hz}, 200 \mathrm{~Hz} \mathrm{LF}$ limit |
| Measurement consideration (frequency and period): All frequency counters are susceptible to error when measuring low-voltage, low-frequency signals. Shielding inputs from external noise pickup is critical for minimizing measurement errors. |  |
| DC current |  |
| Shunt resistance | $5 \Omega$ for $10 \mathrm{~mA}, 100 \mathrm{~mA} ; 0.1 \Omega$ for 1 A |


| True RMS AC current |  |
| :---: | :---: |
| Measurement method | Direct-coupled to the fuse and shunt. AC coupled True RMS measurement (measures the ac component only |
| Shunt resistance | $5 \Omega$ for $10 \mathrm{~mA} ; 0.1 \Omega$ for $100 \mathrm{~mA}, 1 \mathrm{~A}$ |
| Input protection | 1.5 A $250 \mathrm{Vac}, 50 / 60 \mathrm{~Hz}$ fuse on 34921A module |
| Thermocouple |  |
| Conversion | ITS-90 software compensation |
| Reference junction type | Internal, fixed, or external |
| Open thermocouple check | Selectable per channel. Open > 5 k |
| Thermistor | 44004, 44007, 44006 series |
| RTD | $\mathrm{a}=0.00385$ (DIN) and $\mathrm{a}=0.00392$ |
| Measurement noise rejection $60 / 50 \mathrm{~Hz}{ }^{1}$ |  |
| DC CMRR AC CMMR | $\begin{aligned} & 140 \mathrm{~dB} \\ & 70 \mathrm{~dB} \end{aligned}$ |
| Integration time Normal mode rejection ${ }^{2}$ |  |
| $\begin{aligned} & 200 \mathrm{plc} / 3.33 \mathrm{~s}(4 \mathrm{~s}) \\ & 100 \mathrm{plc} / 1.67 \mathrm{~s}(2 \mathrm{~s}) \\ & 20 \mathrm{plc} / 333 \mathrm{~ms}(400 \mathrm{~ms}) \\ & 10 \mathrm{plc} / 167 \mathrm{~ms}(200 \mathrm{~ms}) \\ & 2 \mathrm{plc} / 33.3 \mathrm{~ms}(40 \mathrm{~ms}) \\ & 1 \mathrm{plc} / 16.7 \mathrm{~ms}(20 \mathrm{~ms}) \\ & <1 \mathrm{plc} \end{aligned}$ | $\begin{aligned} & 105 \mathrm{~dB}^{3} \\ & 100 \mathrm{~dB}^{3} \\ & 95 \mathrm{~dB}^{3} \\ & 90 \mathrm{~dB}^{3} \\ & 85 \mathrm{~dB} \\ & 60 \mathrm{~dB} \\ & 0 \mathrm{~dB} \end{aligned}$ |
| Note: <br> 1. For $1 \mathrm{k} \Omega$ unbalance <br> 2. For power line frequ <br> 3. For power line frequ | 08\% <br> use 75 dB or $\pm 2.5 \%$ use 60 dB |

## DC operating characteristics ${ }^{4}$

| Function | Digits ${ }^{5}$ | Readings / s | Additional RMS noise error |
| :---: | :---: | :---: | :---: |
| DCV ${ }^{7}$, DCI, and Resistance ( $\leq 10 \mathrm{k} \Omega$ ) | $\begin{aligned} & 61 / 2 \\ & 61 / 2 \\ & 51 / 2 \\ & 51 / 2 \\ & 41 / 2 \\ & 4^{1 / 2} 2 \end{aligned}$ | $\begin{aligned} & 0.6(0.5) \\ & 6(5) \\ & 60(50) \\ & 300 \\ & 600 \\ & 3000 \end{aligned}$ | $0 \%$ of range $0 \%$ of range 0.001\% of range $0.001 \%$ of range ${ }^{6}$ $0.01 \%$ of range ${ }^{6}$ $0.1 \%$ of range ${ }^{6}$ |
| Autozero OFF operation |  |  |  |
| Following instrument warm-up at calibration temperature $\pm 1^{\circ} \mathrm{C}$ and $<10$ minutes, add $0.0002 \%$ range additional error. $+5 \mu \mathrm{~V}$. (For 300 VDC , instead of $.0002 \%$ of range, need $.00066 \%$ of range) |  |  |  |
| Settling considerations |  |  |  |
| Reading settling times are affected by source impedance, low dielectric absorption characteristics, and input signal changes. |  |  |  |
| AC operating characteristics ${ }^{8}$ | Digits ${ }^{9}$ | Readings / | AC Filter |
| ACV, ACI: | $\begin{aligned} & 61 / 2 \\ & 61 / 2 \\ & 61 / 2 \\ & 61 / 2 \end{aligned}$ | $\begin{aligned} & 7 \text { sec/reading } \\ & 1 \\ & 8^{10} \\ & 100^{11} \end{aligned}$ | Slow (3 Hz) <br> Medium (20Hz) <br> Fast ( 200 Hz ) <br> Fast ( 200 Hz ) |

## Note:

[^0]
## General Specifications

System specifications

| Scanning inputs |  |
| :---: | :---: |
| Analog: | 34921A, 34922A, 34923A, 34924A, and 34925A multiplexer channels |
| Digital | 34950A/52A digital in and totalize |
| Scan triggering |  |
| Source | Interval, external, button press software, or on monitor channel alarm |
| Scan count | 1 to 50,000 or continuous |
| Scan Interval | 0099 hours; t ms step size |
| Channel delay | 0 to 60 seconds per channel; 1 ms step size |
| External trig delay | $<2 \mathrm{~ms}$. With monitor on < 200 ms |
| External trig jitter | $<2 \mathrm{~ms}$ |
| Alarms |  |
| Analog inputs | $\mathrm{Hi}, \mathrm{Lo}$, or $\mathrm{Hi}+$ Lo evaluated each scan |
| Digital inputs | 34950A/52A digital in maskable pattern match or state change 34950A/52A frequency and totalize: Hi limit only |
| Monitor channel | Alarm evaluated each reading |
| Alarm outputs | 4 TTL compatible. Selectable TTL logic Hi or Lo on fail |
| Latency | 5 ms (typical) |
| Memory |  |
| Type | Volatile |
| Readings | 500,000 with timestamp, readable during scan |
| States | 5 instrument states with user label |
| Alarm queue | Up to 20 events with channel number, reading, and timestamp |
| System features |  |
| Per-channel | Individual Mx+B scaling and calculated real-time min/max/average |
| Power fail recovery | Save switch states |
| Relay maintenance | Counts each relay closure and stores on module. User resettable |
| Real-time clock | Battery-backed, 20-year typical life |
| Line power |  |
| Power supply | Universal 100 Vac to 240 Vac |
| Power line frequency | $50-60 \mathrm{~Hz} \pm 10 \%$ automatically sensed |
| Power consumption | 150 VA |
| Environment |  |
| Operating environment | - $0-55^{\circ} \mathrm{C}, 2000 \mathrm{~m}$, maximum humidity is $80 \% \mathrm{RH}$ at $40^{\circ} \mathrm{C}$ and decreases linearly to $37 \% \mathrm{RH}$ at $55^{\circ} \mathrm{C}^{2}$ <br> - IEC 60664-1 pollution degree 1 or 2 <br> - Refer to 34980A User's Guide, Chapter 4, Module Considerations section for the details of Environmental Operating Limits (current and power dissipation) for the Plug-In Modules in Pollution degree 1 or 2 environments. |
| Storage environment | $-40^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}{ }^{1}$ |
| Note: <br> 1. Storage at tempe <br> 2. From $40^{\circ} \mathrm{C}$ to 55 | above $40^{\circ} \mathrm{C}$ will decrease battery life <br> maximum \% Relative Humidity follows the line of constant dew point. |


| Mechanical |  |
| :---: | :---: |
| Mainframe dimensions | $133 \text { H x } 426 \text { W x } 341 \text { D mm (5.25" x 16.8" x 14") }$ <br> Full rack, 3 units high |
| Mainframe weight | 8.8 kg (19.6 lbs) |
| Module dimensions | Approximately 170 W x 270 D x 27 H mm (6.69" $\times 10.62$ " x 1.06") |
| Module weights | 0.73 to $1.18 \mathrm{~kg} \mathrm{(1.6} \mathrm{to} 2.6 \mathrm{lbs}$ |
| Terminal block dimensions | 170 W x 165 [or 105] D x 25 H mm (6.69" x 6.49" [or 4.13"] x 0.98") |
| Terminal block weights | 0.27 to 0.36 kg ( 0.6 to 0.8 lbs ) |
| Regulatory |  |
| Safety and EMC | Refer to Declaration of Conformity for the latest revisions of regulatory compliance at: www.keysight.com/go/conformity |
| Recommended software |  |

- Keysight IO Libraries Suite 2019 or greater: www.keysight.com/find/IOLibraries
- BenchVue: www.keysight.com/find/benchvue
- BenchVue DAQ: www.keysight.com/find/benchvuedaq

The BenchVue platform is a prerequisite for the BenchVue DAQ application.

## Software driver support for programming languages

Software drivers
IVI-C and IVI COM for Windows 7/8.1/10 and LabVIEW
Compatible programming tools and environments

- Keysight VEE Pro
- National Instruments Test Stand, Measurement Studio, Lab Windows/CVI, LabVIEW, Switch Executive
- Microsoft Visual Studio.NET, C/C++, Visual Basic


## Keysight BenchVue Data Acquisition Control and Analysis Application system requirements

| Operating system | - Microsoft Windows 10: 32-bit \& 64-bit (Professional, Enterprise, Education, Home versions). <br> - Microsoft Windows 8 \& 8.1: 32-bit \& 64-bit (Professional, Enterprise, Core). <br> - Microsoft Windows 7: SP1 and later 32-bit \& 64-bit (Professional, Enterprise, Ultimate, Home Basic, Home Premium) |
| :---: | :---: |
| Processor | 1 GHz or faster (2 GHz or greater recommended) |
| RAM | RAM: 1 GB (32-bit) or 2 GB (64-bit) (3 GB or greater recommended) |
| Display | 1280x1024 minimum for single instrument view (higher resolution recommended for multiple instrument view) |
| Computer Interfaces |  |
| LXI | 10/100Base-T Ethernet (Sockets, VXI-11 protocol, Web user interface) |
| USB | Standard USB 2.0 |
| GPIB | IEEE-488 |
| Language | SCPI-1999, IEEE-488.2 |

## Definitions for Specifications

Specifications describe the warranted performance of calibrated instruments that have been stored for a minimum of 2 hours within the operating temperature range of $0^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$, unless otherwise stated, and after a 45-minute warm-up period. Data represented in this document are specifications unless otherwise noted.

Characteristics describe product performance that is useful in the application of the product but that is not covered by the product warranty. Characteristics are often referred to as Typical or Nominal values.

- Typically describes the characteristic performance, which $80 \%$ of instruments will meet when operated over a $20^{\circ} \mathrm{C}$ to $30^{\circ} \mathrm{C}$ temperature range. Typical performance is not warranted.
- Nominal describes representative performance that is useful in the application of the product when operated over a $20^{\circ} \mathrm{C}$ to $30^{\circ} \mathrm{C}$ temperature range. Nominal performance is not warranted.


## Note: All graphs contain measured data from several units at room temperature unless otherwise noted.

## Ordering Instructions

## Mainframe - holds up to 8 plug-in modules

| Mainframe | Description | Remarks |
| :---: | :---: | :---: |
| 34980A | Multifunction data acquisition mainframe | Comes standard with "DMM" option and BenchVue DAQ |
| BV0006B | BenchVue Data Acquisition Control and Analysis (DAQ) | BenchVue makes it simple to connect, control instruments, and automate test sequences. Supported plug-in modules: <br> 34921A, 34922A, 34923A, 34924A, 34925A, 34931A, 34932A, 34933A, 34937A, 34938A, 34952A. |


| Module | Description | Connectors | Optional terminal blocks, cables, connector kits |
| :---: | :---: | :---: | :---: |
| Multiplexer modules |  |  |  |
| 34921A | 40-channel armature multiplexer w/low thermal offset <br> (Order 34921T for temp reference) | $\begin{aligned} & 2-50 \text {-pin } \\ & \text { Dsub, Male } \end{aligned}$ | 3492xT Terminal block with screw connectors <br> Y1135A - 1.5 m 50-pin M/F Dsub cable <br> Y1136A - 3 m 50-pin M/F Dsub cable <br> Y1139A - 50-pin female solder cup connector kit |
| 34923A | 40/80-channel reed multiplexer |  |  |
| 34925A | 40/80-channel optically isolated FET multiplexer |  |  |
| 34922A | 70-channel armature multiplexer | $\begin{aligned} & \hline 2-78 \text {-pin } \\ & \text { Dsub, Male } \end{aligned}$ | 3492xT Terminal block, option 001 for solder connections, option 002 for screw connectors Y1137A - 1.5 m 78-pin M/F Dsub cable Y1138A - 3 m 78-pin M/F Dsub cable Y1140A - 78-pin female solder cup connector kit |
| 34924A | 70-channel reed multiplexer |  |  |
| Matrix modules |  |  |  |
| 34931A | Dual $4 \times 8$ armature matrix | $\begin{aligned} & 2-50 \text {-pin } \\ & \text { Dsub, Male } \end{aligned}$ | 3493xT Terminal block with screw connectors <br> Y1135A - 1.5 m 50-pin M/F Dsub cable <br> Y1136A - 3 m 50-pin M/F Dsub cable <br> Y1139A - 50-pin female solder cup connector kit |
| 34932A | Dual $4 \times 16$ armature matrix |  |  |
| 34933A | Dual/Quad 4x8 reed matrix |  |  |
| 34934A | Quad 4x32 reed matrix | $\begin{aligned} & \hline 2-78-\text { pin } \\ & \text { Dsub, Male } \end{aligned}$ | 34934T Terminal block with screw connectors OR 34934C Configuration block <br> Y1137A - 1.5 m 78-pin M/F Dsub cable Y1138A - 3 m 78-pin M/F Dsub cable <br> Y1140A - 78-pin female solder cup connector kit |
| General-purpose/actuator modules |  |  |  |
| 34937A | 32-channel Form C/Form A generalpurpose switch | $\begin{aligned} & 2-50 \text {-pin } \\ & \text { Dsub, Male } \end{aligned}$ | 3493xT Terminal block with screw connectors <br> Y1135A - 1.5 m 50-pin M/F Dsub cable <br> Y1136A - 3 m 50-pin M/F Dsub cable <br> Y1139A - 50-pin female solder cup connector kit |
| 34938A | 20-channel 5-amp Form A switch |  |  |
| 34939A | 64-channel Form A Switch | $\begin{aligned} & \hline 2-78-\text { pin } \\ & \text { Dsub, Male } \end{aligned}$ | 34939T Terminal block with screw connectors <br> Y1137A - 1.5 m 78 -pin M/F Dsub cable <br> Y1138A - 3 m 78-pin M/F Dsub cable <br> Y1140A - 78-pin female solder cup connector kit |
| RF and microwave modules |  |  |  |
| 34941A | Quad 1x4 50-ohm 3-GHz RF multiplexer | $20-$ SMA | Requires standard 50 ohm SMA RF cables, and optional 8710-2576 SMA Extender wrench for connecting SMA connectors |
| 34942A | Quad 1x4 75-ohm 1.5 GHz RF multiplexer | $\begin{aligned} & \hline 20-\text { mini } \\ & \text { SMB } \end{aligned}$ | Requires mini 75-ohm SMB RF cables, adapters |
| 34945A | Microwave switch/attenuator driver | N/A | Requires 34945EXT and optional Y1150A-Y1155A distribution boards (one 34945EXT is automatically included in each 34945A order) |
| 34946A | Dual 1x2 SPDT terminated microwave switch <br> Option 001: No switches installed Option 004: 4 GHz switches installed Option 020: 20 GHz switches installed Option 026: 26.5 GHz switches installed | 6 SMA | Requires standard 50 ohm SMA cables and adapters 8710-2576 SMA Extender for connecting SMA connectors. <br> Option 001 supports qty 2 of any of the following switches: <br> N1810UL unterminated SPDT <br> N1810TL terminated SPDT <br> N1811TL terminated 4 port transfer <br> N1812UL unterminated 5 port transfer |
| 34947A |  | 9 SMA | Requires standard 50 ohm SMA cables and adapters 8710-2576 SMA Extender for connecting SMA connectors. Option 001 supports qty 3 N1810UL unterminated SPDT switches |


| System measurement and control modules |  |  |  |
| :---: | :---: | :---: | :---: |
| 34950A | 64-bit digital I/O with memory and counter | $2-78 \text {-pin }$ <br> Dsub, <br> Female | 3495xT Terminal block with screw connectors <br> Y1134A - Row expansion cables <br> Y1137A - 1.5 m 78 -pin M/F Dsub cable <br> Y1138A - 3 m 78 -pin M/F Dsub cable <br> Y1142A - 78-pin male solder cup connector kit |
| 34951A | 4-channel isolated D/A converter with waveform memory <br> (DMM option required for calibration) | $1-50 \text {-pin }$ <br> Dsub, <br> Female | $3495 \times$ T Terminal block with screw connectors Y1135A - 1.5 m 50-pin M/F Dsub cable <br> Y1136A - 3 m 50-pin M/F Dsub cable |
| 34952A | Multifunction module with 32-bit DIO, 2ch D/A, and totalizer |  | Y1141A - 50-pin male solder cup connector kit |
| 34959A | Breadboard component module | 26- \& 40-pin internal ribbon cable connectors | Any terminal block can be used assuming 50- or 78-pin Dsub is used ${ }^{1}$ |

Accessories


[^1]
## Accessories

| 34945A ac each 64 co | ssory distribution boards are required for control of external switches. One 34945EXT is required for s (included, add more 34945EXTs for additional coils). |
| :---: | :---: |
| 34945EXT | External driver for 34945A, one required for each 64 coils -holds 4 distribution boards. Order Y1157A-Y1159A cable kits to connect from distribution boards to switches and attenuators |
| Y1150A | 34945EXT distribution board for 8 N181x SPDT switches |
| Y1151A | 34945EXT distribution board for two 87104x/106x L7x0xx multiport or 87406B matrix switches |
| Y1152A | 34945EXT distribution board for one 87204x/206x or 87606B switch and two N181x switches |
| Y1153A | 34945EXT distribution board for two 84904/5/6/7/8 or 8494/5/6 step attenuators |
| Y1154A | 34945EXT distribution board for two 87222, L7222C transfer switches and six N181x SPDT switches |
| Y1155A | 34945EXT distribution board w/ generic screw terminals for driving 16 switch coils |
| Y1157A | 9-to-10 pin cable kit for Y1150A, Y1152A, Y1154A - supplies to build 4 cables |
| Y1158A | 10-to-10/10-to-14 pin cable kit for Y1153A, Y1154A - supplies to build 2 cables |
| Y1159A | 6-to-16 pin cable kit for 1150A/51A/52A/53A/54A/55A - supplies to build 2 cables |
| Thermocouples/thermistors |  |
| 34307A | 10 pack of J type thermocouples |
| 34308A | 5 pack of 10 k thermistors |

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[^0]:    Reading speeds for 60 Hz and ( 50 Hz ) operation; autozero OFF
    $61 / 2$ digits $=22$ bits; $51 / 22$ digits $=18$ bits; $41 / 2$ digits $=15$ bits Add $20 \mu \mathrm{~V}$ for DCV, $4 \mu \mathrm{~A}$ for DCI , or $20 \mathrm{~m} \Omega$ for resistance For 300 VDC, multiply the additional noise error by 3.3
    5. Maximum reading rates for $0.01 \%$ of AC step additional error; additional settling delay required when input DC level varies
    $61 / 2$ digits $=22$ bits; $51 / 2$ digits $=18$ bits; $41 / 2$ digits $=15$ bits
    7. For external trigger or remote operation using default settling delay (Delay Auto)
    8. Maximum limit with default settling delays defeated

[^1]:    1. Refer to the list of Conditions of Acceptability to determine suitability of the combination. See page 33 for more information.
