USB-6423 Specifications



Ihr Ansprechpartner / Your Partner:

dataTec AG

E-Mail: info@datatec.eu >>> www.datatec.eu



Authorized Distributor



USB-6423 Specifications 16-Bit, 250 kS/s (32 AI, 4 AO, 16 DIO) mioDAQ Multifunction I/O Device

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Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

Characteristics describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- Typical specifications describe the performance met by a majority of models.
- Nominal specifications describe an attribute that is based on design, conformance testing, or supplemental testing.

Specifications are *Typical* unless otherwise noted.

Conditions

Specifications are valid at 25 °C unless otherwise noted.

USB-6423 AI Connector Pinout

Use the pinout to connect to analog input terminals on the USB-6423.

Figure 1: USB-6423 Al Connector Pinout

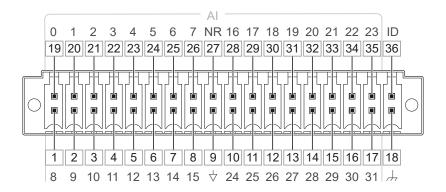


Table 1: USB-6423 Al Connector Pin Assignments

Pin	Signal
1	AI 8
2	AI 9
3	AI 10
4	AI 11
5	AI 12
6	AI 13
7	AI 14
8	AI 15
9	AI GND
10	AI 24
11	AI 25
12	AI 26
13	AI 27
14	AI 28
15	AI 29
16	AI 30
17	Al 31
18	CHSGND
19	AI 0
20	Al 1
21	AI 2
22	AI 3
23	AI 4
24	AI 5
25	AI 6
26	AI 7
27	NR (AI SENSE)

Table 1: USB-6423 Al Connector Pin Assignments (Continued)

Pin	Signal
28	AI 16
29	AI 17
30	AI 18
31	AI 19
32	AI 20
33	AI 21
34	AI 22
35	AI 23
36	ID 0

Table 2: USB-6423 AI Connector Signal Descriptions

Signal	Function	Reference	Direction	Description
AI <07>	Analog input Varies		Input	Supports differential or single-ended measurement modes. The default configuration is differential mode. In differential mode, these channels are the positive input for the differential pair. The negative input of the differential pair is located directly beneath the positive input. In single-ended mode, each signal is a separate analog input voltage channel. The
Al <1623>				ground reference in single-ended mode is configurable. In referenced single-ended (RSE) mode, AI GND is the reference for the voltage measurement. In non-referenced single-ended (NRSE) mode, the NR pin is the reference.
				You can configure the input mode per channel.

Table 2: USB-6423 AI Connector Signal Descriptions (Continued)

Signal	Function	Reference	Direction	Description
AI <815> AI <2431>	Analog input channels	Varies	Input	Supports single-ended measurements only. The default configuration is (RSE) mode. In RSE mode, AI GND is the reference for the voltage measurement. In NRSE mode, the NR pin is the reference. For differential measurements, refer to the descriptions for AI <07>.
AI GND	Analog input ground	_	_	The reference point for single-ended measurements in RSE mode and the bias current return point for differential measurements. AI GND, AO GND, D GND, and CHSGND are all connected internally.
NR (AI SENSE)	AI SENSE for NRSE mode	_	Input	The AI SENSE pin is labeled "NR" because it is used when the input terminal is configured to NRSE mode. In NRSE mode, AI SENSE acts as a remote sense of a reference voltage that can be at a different voltage potential than AI GND.
CHSGND	Chassis ground	_	_	Connects directly to the chassis ground of the USB-6423 enclosure. It can be used as a termination point for shielded cables to help improve measurement quality.
ID 0	_	_	_	This feature is not supported yet.

USB-6423 AO/DIO Connector Pinout

Use the pinout to connect to analog output and digital input/output terminals on the USB-6423.

Figure 2: USB-6423 AO/DIO Connector Pinout

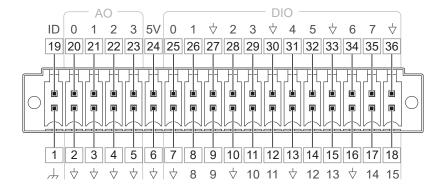


Table 3: USB-6423 AO/DIO Connector Pin Assignments

Pin	Signal
1	CHSGND
2	AO GND
3	AO GND
4	AO GND
5	AO GND
6	D GND
7	D GND
8	PFI 8/P0.8 (port0/line8)
9	PFI 9/P0.9 (port0/line9)
10	D GND
11	PFI 10/P0.10 (port0/line10)
12	PFI 11/P0.11 (port0/line11)
13	D GND
14	PFI 12/P0.12 (port0/line12)

Table 3: USB-6423 AO/DIO Connector Pin Assignments (Continued)

Pin	Signal
15	PFI 13/P0.13 (port0/line13)
16	D GND
17	PFI 14/P0.14 (port0/line14)
18	PFI 15/P0.15 (port0/line15)
19	ID 1
20	AO 0
21	AO 1
22	AO 2
23	AO 3
24	+5 V
25	PFI 0/P0.0 (port0/line0)
26	PFI 1/P0.1 (port0/line1)
27	D GND
28	PFI 2/P0.2 (port0/line2)
29	PFI 3/P0.3 (port0/line3)
30	D GND
31	PFI 4/P0.4 (port0/line4)
32	PFI 5/P0.5 (port0/line5)
33	D GND
34	PFI 6/P0.6 (port0/line6)
35	PFI 7/P0.7 (port0/line7)
36	D GND

Table 4: USB-6423 AO/DIO Connector Signal Descriptions

Signal	Function	Reference	Direction	Description
AO <03>	Analog output channels	AO GND	Output	Supplies the voltage output of the AO channels.

Table 4: USB-6423 AO/DIO Connector Signal Descriptions (Continued)

Signal	Function	Reference	Direction	Description	
AO GND	Analog output ground	_	_	AO GND is the reference for the AO channels. NOTE AI GND, AO GND, D GND, and CHSGND are all connected internally.	
+5 V	+5 V power source	D GND	Output	Provides current limited +5 V power output that can be used to power external circuitry. Refer to the +5 V Power Source section for more information. Leave this pin open if you do not use it.	
P0.<015>	Port 0 digital I/O channels	D GND	Input or output	Digital channels that can be individually configured as input or output. Can also be individually configured for the following uses. Digital I/O Counter/timer input Counter/timer output External timing source for AI/AO/DI/DO/Counter timing signal output from AI/AO/DI/DO/Counter.	
D GND	Digital ground	_	_	Supplies the reference for the P0.<015> pins and +5 V pin. AI GND, AO GND, D GND, and CHSGND are all connected internally.	
CHSGND	Chassis ground	_	_	Connects directly to the chassis ground of the USB-6423 enclosure. It can be used as a termination point for shielded cables to help improve measurement quality.	
ID 1	_	_	_	This feature is not supported yet.	



RELATED INFORMATION

+5 V Power Source

Analog Input

Number of channels	32 single-ended or 16	32 single-ended or 16 differential			
ADC resolution	16 bits	16 bits			
DNL	No missing codes gua	No missing codes guaranteed			
INL	Refer to AI Absolute Ac	Refer to AI Absolute Accuracy			
Sample rate					
Single channel maximun	n	250 kS/s			
Multichannel maximum	(aggregate)	250 kS/s			
Minimum		No minimum			
Timing resolution		10 ns			
Timing accuracy		50 ppm of sa	mple rate		
Input coupling [DC				
Input range	±0.2 V ±1 V ±5 V ±10 V				
Power on state	Differential Mode at 10 V Ra	ange			
Maximum working voltage for a	Maximum working voltage for analog inputs (signal + common mode)				
All input ranges (±0.2V, ±	1 V, ±5 V, ±10 V)		±11 V to AI GND		

Table 5: Input Impedance

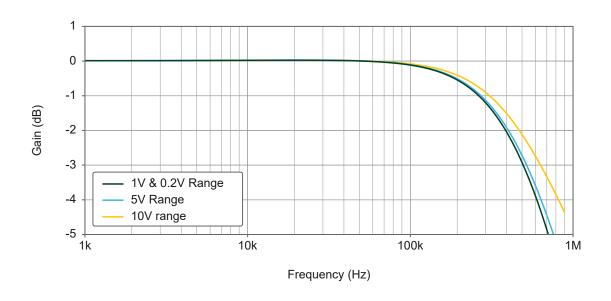
Device on	AI+ to AI GND	>10 GΩ in parallel with 50 pF
Device on	AI- to AI GND	>10 GΩ in parallel with 50 pF
Device off	AI+ to AI GND	1,250 Ω
Device on	AI- to AI GND	1,250 Ω

Input bias current	±30 pA typical ±1 nA maximum over full temperatu	ıre range				
Crosstalk (at 100 kHz)						
Differential channe	els	-65 dB				
Single-ended chan	nels	-50 dB				
Small signal bandwidth (-3 dB)						
Input range ±10 V		630 kHz				
Input range ±5 V		510 kHz				

Input range ±1 V, ±0.2 V

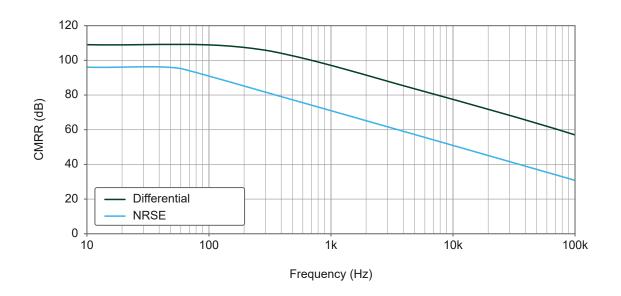
490 kHz

Figure 3: USB-6423 Small Signal Bandwidth versus Frequency



Differential mode	100 dB
Non-referenced single-ended (NRSE) mode	90 dB

Figure 4: USB-6423 CMRR versus Frequency



Input FIFO size	8,191 samples shared among channels used
Scan list memory	4,095 entries
Data transfers	USB Signal Stream, programmed I/O

Overvoltage protection for AI<AI0...31> and NR (AI Sense) pins

Device on	±30 V for up to two AI pins
Device off	±20 V for up to two AI pins
Input current during overvoltage condition	±16 mA maximum per AI pin ±16 mA maximum per NR pin

Table 6: Settling Time for Multi-channel Measurements at Full-Scale Step

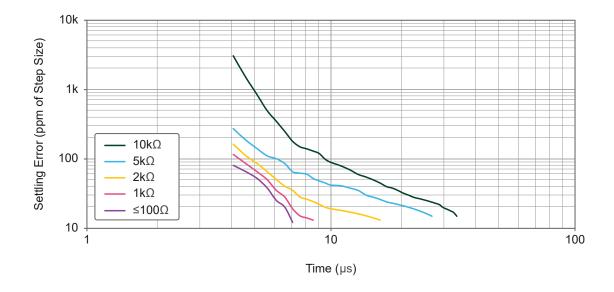
Input Range	±90 ppm (6 LSB)	±30 ppm (2 LSB)	±15 ppm (1 LSB)
$\pm 0.2 \text{ V}, \pm 1 \text{ V}, \pm 5 \text{ V}, \pm 10 \text{ V}$	4 μs	6 μs	7 μs



NOTE

Refer to the Multi-channel Scanning Considerations section in the USB-6423 User *Manual* for the best settling time performance.

Figure 5: USB-6423 Settling Error versus Time for Different Source Impedances





RELATED INFORMATION

Multi-channel Scanning Considerations

Al Absolute Accuracy (Warranted)



NOTICE

The input channels of the USB-6423 are sensitive to electromagnetic interference (EMI). As a result, you might experience reduced measurement accuracy or temporary performance degradation with cables routed through strong EMI environments. To ensure optimal performance, either avoid such environments, or carefully select and route cables or probes connected to the USB-6423.

Table 7: Al Absolute Accuracy

Nominal Range, Positive Full Scale (V)	Range, Negative	Gain Error (ppm of	10 Years Residual Gain Error (ppm of Reading)	Tempco (ppm of Range/°C)	Residual Offset Error (ppm of Range)	Tempco (ppm of	Noise, σ (μV	Absolute	10 Years Absolute Accuracy at Full Scale (µV)
10	-10	102	172	15	21	40	240	2,607	3,307
5	-5	110	180	15	23	40	122	1,354	1,704
1	-1	110	180	15	26	44	28	278	348
0.2	-0.2	122	192	15	78	60	13	72	86



NOTE

Absolute accuracy at full scale on the analog input channels is determined using the following assumptions:

- Temp Change From Last External Cal = 10 °C
- Temp Change From Last Internal Cal = 1 °C
- Number of readings = 10,000
- Coverage Factor = 3σ



NOTE

Accuracies listed are valid for up to 2 and 10 years from the device external calibration.

Reference Tempco	5 ppm/°C
INL error	32 ppm of range

AI Absolute Accuracy Equation

Absolute Accuracy = Reading * (Gain Error) + Range * (Offset Error) + Noise Uncertainty

- (Temp Change From Last External Cal)
- $Offset\ Error = Residual\ Offset\ Error + Offset\ Tempco\ ^* \ (Temp\ Change\ From\ Last\ Internal\ Cal)\ +\ INL\ Error$
- Random Noise * 3 Noise Uncertainty = $\sqrt{10,000}$

For a coverage factor of 3 σ and averaging 10,000 points.

AI Absolute Accuracy Example

For example, on the 10 V range for 2 years calibration interval, the absolute accuracy at full scale is as follows:

- Gain Error: 102 ppm + 15 ppm * 1 + 5 ppm * 10 = 167 ppm
- Offset Error: 21 ppm + 40 ppm * 1 + 32 ppm = 93 ppm
- Noise Uncertainty: $\frac{240~\mu V~^*~3}{\sqrt{10,000}}~=~7.2~\mu V$
- Absolute Accuracy: 10 V * (Gain Error) + 10 V * (Offset Error) + Noise Uncertainty = 2,607 μ V

Analog Output

Number of channels	4
DAC resolution	16 bits
DNL	±1 LSB
Monotonicity	16 bits guaranteed
Maximum update rate (simultaneous)	
All channels	05010/
Att charmets	250 kS/s
Timing accuracy	50 ppm of sample rate

Output range	±10 V
Output coupling	DC
Output impedance ¹	0.05 Ω
Output current drive	±2 mA
Overdrive protection during power on/off	±30 V
Overdrive current	2.8 mA
Power-on state	Less than ±5 mV
Output FIFO size	16,383 samples shared among channels used
Data transfers	USB Signal Stream, programmed I/O
AO waveform modes	Non-periodic waveform Periodic waveform regeneration mode from onboard FIFO Periodic waveform regeneration from host buffer, including dynamic update
Settling time, full-scale step, 15 ppm (1 LSB)	25 μs with 50 pF load
Slew rate	8 V/μs
AO glitch	
Device power up	±0.9 V peak for 8 ms
Device power down, reset, or U	SB hot unplug -1.4 V peak for 400 ns
Glitch energy mid-scale code transitio	en ±5 mV for 5 μs
Crosstalk (at 10 kHz)	<-100 dB

AO Absolute Accuracy (Warranted)



NOTICE

The output channels of the USB-6423 are sensitive to electromagnetic interference (EMI). As a result, you might experience reduced measurement accuracy or temporary performance degradation with cables routed through strong EMI environments. To ensure optimal performance, either avoid such environments, or carefully select and route cables or probes connected to the USB-6423.

Absolute accuracy at full-scale numbers is valid immediately following internal calibration and assumes the device is operating within 10 °C of the last external calibration.

Output impedance excludes cabling impedance.

Table 8: AO Absolute Accuracy

Positive	Negative	Gain	_	Gain Tempco (ppm of Range/°C)	Error	,	Absolute Accuracy at Full	10 Years Absolute Accuracy at Full Scale (µV)
10	-10	106	176	17	34	40.7	2,787	3,487

NOTE

Accuracies listed are valid for up to 2 and 10 years from the device external calibration.

Reference Tempco	5 ppm/°C
INL error	31 ppm of range

AO Absolute Accuracy Equation

Absolute Accuracy = Output Value * (Gain Error) + Range * (Offset Error)

- Gain Error = Residual Gain Error + Gain Tempco *
 (Temp Change From Last Internal Cal) + Reference Tempco *
 (Temp Change From Last External Cal)
- Offset Error = Residual Offset Error + Offset Tempco * (Temp Change From Last Internal Cal) + INLError

Digital I/O (PFI)

Number of channels 16

Capabilities	Static Digital I/O, Waveform Digital I/O, PFI, Counter, or Timer
Direction control	Each terminal can be programmed individually as input or output
Logic Family	5 V (LVCMOS)

Electrical Characteristics

Ground reference	D GND
Direction control	Program each as input or output individually
Pull-down resistor	47 kΩ

Input voltage protection	±20 V per line, up to two lines simultaneously
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NOTICE

Stresses beyond those listed under the Input voltage protection specification may cause permanent damage to the USB-6423.

Static Digital I/O Capabilities

Channel names in software	Port0/line0:15
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Waveform Digital I/O Capabilities

Channel names in software	Port0/line0:15
Port/sample size	Up to 16 bits
Waveform generation (DO) FIFO	8,191 samples
Waveform acquisition (DI) FIFO	1,023 samples
DO or DI sample clock frequency	0 MHz to 10 MHz, system and bus activity dependent
Data transfers	USB Signal Stream, programmed I/O

PFI Functionality

Channel names in software	PFI0:15
Functionality	Timing input Timing output
Timing output sources	Many AI, AO, counter, DI, and DO timing signals

Recommended Operating Conditions

Output high current (IOH)

3 3 3 3 4 6 6 7 7	
DIO<015>	-10 mA maximum per channel
Output low current (I _{OL})	
DIO<015>	10 mA maximum per channel



The maximum output current is shared between all channels and the +5 V power

Table 9: Digital Input Logic Levels

Logic Family	Input Low Voltage (V _{IL})		Input High Voltage (V _{IH})	
Logic Family	Minimum	Maximum	Minimum	Maximum
5.0 V	-0.5 V	1.46 V	3.66 V	5.5 V

Table 10: Digital Output Logic Level

Logic Family	Current	Output Low Voltage (V _{OL}) Maximum	Output High Voltage (V _{OH}) Minimum
5.0 V	4 mA	0.30 V	4.59 V

Digital I/O Characteristics

I _{IL} input low current (V _{IN} = 0 V)	-1 μA maximum
I _{IH} input low current (V _{IN} = 5 V)	110 μA maximum

Figure 6: I_{OH} versus V_{OH} , 5.0 V Logic Family

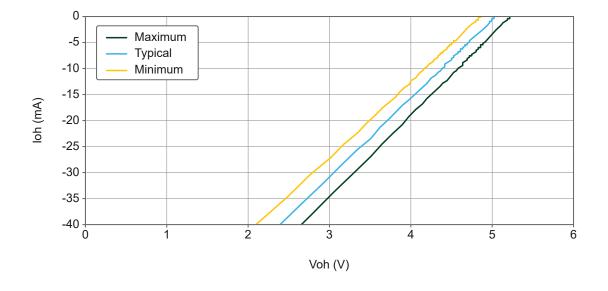
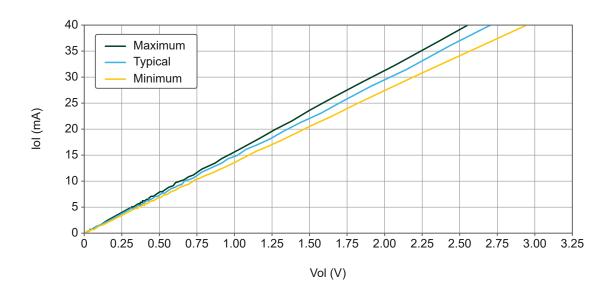


Figure 7: I_{OL} versus V_{OL} , 5.0 V Logic Family



General-Purpose Counters

Number of counters/timers	4
Resolution	32 bits
Counter measurements	Edge counting Pulse Pulse width Semi-period Period Two-edge separation
Position measurements	X1, X2, X4 quadrature encoding with Channel Z reloading Two-pulse encoding
Output applications	Pulse Pulse train Frequency division Equivalent time sampling
Internal base clocks	100 MHz 20 MHz 100 kHz
External base clock frequency	0 MHz to 25 MHz
Base clock accuracy	50 ppm

Gate
Source
HW_Arm
Aux
Inputs
A
B
Z

Up_Down Sample Clock

Routing options for inputs Any PFI, many internal signals

Data transfers Programmed I/O

Frequency Generator

Number of channels 1

Base clocks	100 MHz 20 MHz 100 kHz
Divisors	1 to 16
Base clock accuracy	50 ppm

Output can be available on any PFI terminal.

Bus Interface

USB compatibility

USB 3.0/USB 3.1 Gen 1/USB 3.2 Gen 1 SuperSpeed or USB 2.0

Hi-Speed²

USB Signal Stream

8, can be used for analog input, analog output, digital input, or digital output

USB connector

USB Type-C

+5 V Power Source

Voltago accuracy	No load	+4.87 V to +5.22 V
Voltage accuracy	Maximum current	+4.76 V to 5.17 V

Operating on a Hi-Speed bus results in lower performance, and you might not be able to achieve maximum sampling/update rates.

Maximum load current³

Connected to USB 2.0 Hi-Speed port with	2.5 W power	50 mA
Connected to USB 3.0 SuperSpeed port with ≥4.5 W power		280 mA
Power on state	Always on (no use	r control)
Overdrive protection during power on/off	±30 V	

Power Requirements



CAUTION

The protection provided by the USB-6423 can be impaired if it is used in a manner not described in the USB-6423 User Manual.

USB power rating	4.0 W (800 mA at nominal 5 V)
Power input mating connector	USB Type-C plug for power and data



NOTE

The USB-6423 could function with a USB 2.0 port with 2.5 W power capability, but you will need to limit the I/O utilization. For example, you could reduce the load current of the four analog output channels to 1 mA.



RELATED INFORMATION

USB-6423 User Manual

Current Limit

DIO and +5 V terminals combined [†]	Connected to USB 2.0 Hi-Speed port with 2.5 W power	50 mA
	Connected to USB 3.0 SuperSpeed port with ≥4.5 W power	280 mA

The USB-6423 will self-detect the power capability of the USB host to configure the current limit. If the USB-6423 is at 280 mA limit, it will lower the current limit to 50 mA if there is an overdrive or fault condition. The current limit will be reset back to the default 280 mA limit when the fault or load is removed.

The USB-6423 will self-detect the power capability of USB host to configure the current limit. If the USB-6423 is at 280 mA limit, it will lower the current limit to 50 mA if there is overdrive or fault condition. The current limit will be reset back to the default 280 mA limit when the fault or load is removed.

Maximum Working Voltage

Maximum working voltage refers to the signal voltage plus the common-mode voltage.

Channel to earth

11 V, Measurement Category I

Measurement Category

This product is rated for Measurement Category I.



Do not connect the product to signals or use for measurements within Measurement Categories II, III, or IV.



REMARQUE

Ne pas connecter le produit à des signaux dans les catégories de mesure II, III ou IV et ne pas l'utiliser pour effectuer des mesures dans ces catégories.

Measurement Category I is for measurements performed on circuits not directly connected to the electrical distribution system referred to as MAINS voltage. MAINS is a hazardous live electrical supply system that powers equipment. This category is for measurements of voltages from specially protected secondary circuits. Such voltage measurements include signal levels, special equipment, limited-energy parts of equipment, circuits powered by regulated low-voltage sources, and electronics.



NOTE

Measurement Categories CAT I and CAT O are equivalent. These test and measurement circuits are for other circuits not intended for direct connection to the MAINS building installations of Measurement Categories CAT II, CAT III, or CAT

Physical Characteristics

I/O connector	2x 36-position spring terminals
Dimensions	116.7 mm x 177.0 mm x 30.4 mm (4.59 in. x 6.97 in. x 1.20 in.)
Weight	590 g (1.30 lb)

Field Wiring Specifications

Use copper wiring for all connections unless otherwise stated.

Gauge	0.14 mm ² to 1.5 mm ² (26 AWG to 16 AWG) copper conductor wire	
Wire strip length	10 mm (0.394 in.) of insulation stripped from the end	
Temperature rating	-25 °C to 120 °C	
Wires per terminal	One wire per spring terminal; two wires per spring terminal using a 2-wire ferrule	
Ferrules		
Single ferrule	e, uninsulated	0.14 mm ² to 1.5 mm ² (26 AWG to 16 AWG) 10 mm barrel length
Single ferrule	e, insulated	0.14 mm ² to 1.0 mm ² (26 AWG to 18 AWG) 12 mm barrel length
Two-wire fer	rule, insulated	2x 0.34 mm ² (22 AWG) 12 mm barrel length
Connector securement		
Securement	type	Screw flanges
Torque for so	rew flanges	0.2 N ⋅ m (1.80 lb ⋅ in.)

Environmental Characteristics

Temperature	
Operating temperature	0 °C to 55 °C
Storage temperature	-20 °C to 70 °C
Humidity	
Operating humidity ⁴	10% RH to 90% RH, noncondensing
Storage humidity	5% RH to 95% RH, noncondensing
Pollution Degree	2
Maximum altitude	2,000 m
Shock and vibration	
Operating vibration	5 Hz to 500 Hz, 0.3 g RMS
Non-operating vibration	5 Hz to 500 Hz, 2.4 g RMS

The USB-6423 will perform at the full accuracy specification up to 90% RH operating humidity at ≤40 °C.

Operating shock

30 g, half-sine, 11 ms pulse

Calibration

Recommended warm-up time	15 minutes
Recommended calibration interval	2 years



Ihr Ansprechpartner / Your Partner:

dataTec AG

E-Mail: info@datatec.eu

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